

STIC Search Report

EIC 2100

STIC Database Tracking Number: 116152

TO: Kenneth Tang
Location:
Art Unit : 2127
Friday, March 05, 2004

Case Serial Number: 09/328828

From: Geoffrey St. Leger
Location: EIC 2100
PK2-4B30
Phone: 308-7800

geoffrey.stleger@uspto.gov

Search Notes

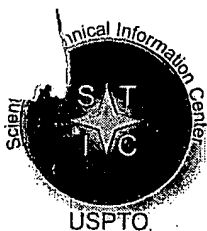
Dear Examiner Tang,

Attached please find the results of your search request for application 09/328828. I searched Dialog's foreign patent files, technical databases, product announcement files and general files; along with ACM and the Internet.

Please let me know if you have any questions.

Regards,

Geoffrey St. Leger
4B30/308-7800



STIC EIC 2100 116152

Search Request Form

Today's Date:

3/5/04

What date would you like to use to limit the search?

Priority Date: 6/8/99

Other:

Name Kenneth Tang

AU 2127 Examiner # 79455

Room # 5A10 Phone 305-5334

Serial # 09/328828

Format for Search Results (Circle One):

PAPER

DISK

EMAIL

Where have you searched so far?

USP

DWPI

EPO

JPO

ACM

IBM TDB

IEEE

INSPEC

SPI

Other

Is this a "Fast & Focused" Search Request? (Circle One) YES NO

A "Fast & Focused" Search is completed in 2-3 hours (maximum). The search must be on a very specific topic and meet certain criteria. The criteria are posted in EIC2100 and on the EIC2100 NPL Web Page at <http://ptoweb/patents/stic/stic-tc2100.htm>.

What is the topic, novelty, motivation, utility, or other specific details defining the desired focus of this search? Please include the concepts, synonyms, keywords, acronyms, definitions, strategies, and anything else that helps to describe the topic. Please attach a copy of the abstract, background, brief summary, pertinent claims and any citations of relevant art you have found.

This is a process control system that is comparing two different processes (or tasks, or jobs, or threads) after both processes have finished executing.

The novelty is that this reduces overhead and is fault-tolerant/fault-detection.

Pertinent claims are 1 and 19.

STIC Searcher Geoffrey St. Leger

Phone 308-7800

Date picked up 3/5/4

Date Completed 3/5/4



File 275:Gale Group Computer DB(TM) 1983-2004/Mar 05
 (c) 2004 The Gale Group
 File 621:Gale Group New Prod.Annou.(R) 1985-2004/Mar 04
 (c) 2004 The Gale Group
 File 636:Gale Group Newsletter DB(TM) 1987-2004/Mar 05
 (c) 2004 The Gale Group
 File 16:Gale Group PROMT(R) 1990-2004/Mar 05
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 File 160:Gale Group PROMT(R) 1972-1989
 (c) 1999 The Gale Group
 File 148:Gale Group Trade & Industry DB 1976-2004/Mar 05
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 File 696:DIALOG Telecom. Newsletters 1995-2004/Mar 04
 (c) 2004 The Dialog Corp.
 File 369:New Scientist 1994-2004/Feb W5
 (c) 2004 Reed Business Information Ltd.

Set	Items	Description
S1	55042	(1ST OR FIRST OR PRIMARY OR INITIAL OR PARENT OR ROOT) (1W) - (PROCESS OR PROCESSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S2	129889	(2ND OR SECOND??? OR CHILD OR SUBSEQUENT OR FOLLOWING OR E- NSUING OR OTHER OR ANOTHER OR DIFFERENT) (1W) (PROCESS OR PROCE- SSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S3	41	(COMPAR? OR CONTRAST? OR WEIGH? OR JUDG? OR MATCH??? OR EV- ALUAT? OR ASSESS? OR MEASUR?) (7N)S1(7N)S2
S4	36	RD (unique items)

4/3,K/1 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

01617079 SUPPLIER NUMBER: 14346192 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Installing traffic lights under Windows NT. (managing multithreading when
programming with Microsoft Windows NT operating system)
(Environments) (Column) (Tutorial)

Putzold, Charles

PC Magazine, v12, n16, p339(6)

Sept 28, 1993

DOCUMENT TYPE: Tutorial ISSN: 0888-8507 LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 3262 LINE COUNT: 00250

... are a bit different. The OS/2 functions DosEnterCritSec and
DosExitCritSec take no parameters. Whenever an OS/2 thread enters a
critical section, all the other threads in the process are halted until
the first thread exits the critical section.

By contrast, the Windows NT critical section mechanism uses the
principle of "mutual exclusion," a term that will come up again as we
continue to explore thread...

4/3,K/2 (Item 2 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

01448351 SUPPLIER NUMBER: 10916476 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Spectrum analyzer self-calibration.

Hillstrom, Timothy L.; Tarantino, Joseph F.

Hewlett-Packard Journal, v42, n3, p47(2)

June, 1991

ISSN: 0018-1153 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 9129 LINE COUNT: 00725

... all of the necessary data structures and much of the documentation
for the entire user interface.

High-level analyzer operation was broken out into five primary
processes: user interface, measurement manager, measurement sequencer,
display manager, and Instrument BASIC. Other processes were allocated
for such purposes as parsing HP-IB input, trace plotting, and other system
utility functions. Each designer was assigned responsibility for one of...

4/3,K/3 (Item 3 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

01437321 SUPPLIER NUMBER: 10916474 (USE FORMAT 7 OR 9 FOR FULL TEXT)
A 10-Hz-to-150-MHz spectrum analyzer with a digital IF section. (includes
related article on adaptive data acquisition)

Carlson, Kirsten C.; Cauthorn, James H.; Hillstrom, Timothy L.; Mason, Roy
L.; Tarantino, Joseph F.; Wardle, Jay M.; Wicklund, Eric J.

Hewlett-Packard Journal, v42, n3, p44(16)

June, 1991

ISSN: 0018-1153 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 10381 LINE COUNT: 00819

... all of the necessary data structures and much of the documentation
for the entire user interface.

High-level analyzer operation was broken out into five primary
processes: user interface, measurement manager, measurement sequencer,
display manager, and Instrument BASIC. Other processes were allocated
for such purposes as parsing HP-IB input, trace plotting, and other system
utility functions. Each designer was assigned responsibility for one of...

4/3,K/4 (Item 1 from file: 621)
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)
(c) 2004 The Gale Group. All rts. reserv.

02705226 Supplier Number: 66570785 (USE FORMAT 7 FOR FULLTEXT)
E Source(TM) Says Outage Communication Essential to Utility Customer Care.
PR Newswire, p7206
Oct 31, 2000
Language: English Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 584

... accurately predicts when it will be back on.
"Outage communication often depends on call center and repair personnel to communicate with other entities as a **secondary task** while they are overwhelmed with their **primary tasks** -taking calls from customers or **assessing** and repairing a troubled distribution system," says E Source(TM) Utility Customer Care Series research associate, Maggie Boys.

"The result," Boys adds, "is that bottlenecks..."

4/3,K/5 (Item 1 from file: 636)
DIALOG(R)File 636:Gale Group Newsletter DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

0063984 Supplier Number: 48145473 (USE FORMAT 7 FOR FULLTEXT)
Kaiser's Earning of Top Quality Grade Serves Dual Marketing Purpose NCQA
Issues Stamp of Approval for Second Time
Healthcare PR & Marketing News, v6, n24, pN/A
Nov 27, 1997
Language: English Record Type: Fulltext
Document Type: Newsletter; Trade
Word Count: 517

... pneumonia immunization programs;
*efforts to decrease C-section procedures;
*screening programs for mammography, cervical cancer and cholesterol;
*telephone access;
*mental health treatment; and
*employee training.
Compared to the first time around, Kaiser's **second review process** was more straight-forward and objective **compared** to the **first review process** which was more subjective. "There was no wiggle room, you either [meet their quality standard] or you don't," said Powell. (Kaiser Permanente of Ohio...

4/3,K/6 (Item 1 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

00814379
Office-automation systems can significantly improve executives' productivity, but personnel and software problems must be overcome before their full potential is realized, according to a study by David A Curtis & Assoc.
EDP Weekly August 2, 1982 p. 1,7+1

Advances in computer and communications technology have facilitated the automation of many **secondary office tasks** such as document preparation, information retrieval, and bookkeeping. In **comparison**, the **primary tasks** performed by managers and executives have been neglected. Primary tasks are mainly intellectual or interpersonal activities. These primary tasks are the most important unfulfilled areas...

4/3,K/7 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

16385731 SUPPLIER NUMBER: 109355772 (USE FORMAT 7 OR 9 FOR FULL TEXT)

A-B-C, easy as 1-2-3: attaching costs to specific tasks and services can maximize your profits and productivity. (Activity-Based Costing)

McGree, Bridget

Industrial Distribution, 92, 10, H1(3)

Oct, 2003

ISSN: 0019-8153

LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 999

LINE COUNT: 00080

... our serves a lower-cost producer by identifying which tasks added value, and which ones didn't," says Doig. "We also improved our quality by measuring primary jobs (the job that the specific employee was hired to handle) versus secondary jobs (other tasks that the employee handles), and were able to rearrange responsibilities."

Doig advises distributors to consider ABC in some shape or form, and says using a...

4/3,K/8 (Item 2 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB

(c)2004 The Gale Group. All rts. reserv.

14558402 SUPPLIER NUMBER: 85467307 (USE FORMAT 7 OR 9 FOR FULL TEXT)

How well do theories of job matching explain variations in job satisfaction across education levels? Evidence for UK graduates.

Belfield, Clive R.; Harris, R.D.F.

Applied Economics, 34, 5, 535(14)

March 20, 2002

ISSN: 0003-6846

LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 9486

LINE COUNT: 01015

... conditional on the worker's information set, which includes information about the characteristics of other jobs and the views of the employer. If information about other jobs is costly, individuals may be more willing to tolerate lower current job satisfaction.

First, job matching models predict the self-employed may be more satisfied to the extent that they have information on their own employment terms. Unions may also provide...

4/3,K/9 (Item 3 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB

(c)2004 The Gale Group. All rts. reserv.

13841045 SUPPLIER NUMBER: 78479836 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Moonlighting: multiple motives and gender differences. (Statistical Data Included)

AVERETT, SUSAN L.

Applied Economics, 33, 11, 1391

Sept 15, 2001

DOCUMENT TYPE: Statistical Data Included

ISSN: 0003-6846

LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 11265

LINE COUNT: 01393

... Diana Dodu provided outstanding research assistance.

(1) However, if an individual moonlights for any reason other than a budget constraint, the wage rate on the second job will not necessarily be lower than the wage rate on the primary job. Shishko and Shishko (1976) discuss comparative statistics for the cases where the wage on the secondary job is less than, equal to or greater than the wage on the primary job.

(2) It is impossible to depict the budget constraints and indifference map for a non-hours constrained moonlighter who has a wage rate on the...

4/3,K/10 (Item 4 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

13473828 SUPPLIER NUMBER: 75102614 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Symbol shifting manufacturing jobs to Mexico. (Brief Article)
CANTOR, MARTIN
LI Business News, 48, 19, 51A
May 11, 2001
DOCUMENT TYPE: Brief Article ISSN: 0894-4806 LANGUAGE: English
RECORD TYPE: Fulltext
WORD COUNT: 559 LINE COUNT: 00047

... to Mexico.

The job cuts will be part of that process. Economists often gauge the economic importance of jobs created by using a multiplier, which **assesses** the **secondary** job impact when a **primary** job is either created or lost. Thus, the manufacturing job loss at Symbol could permanently cost the Long Island economy upwards of 1,200 jobs.

The...

4/3,K/11 (Item 5 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

1347285 SUPPLIER NUMBER: 67882379 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Psychoanalysis and Sexual Fantasies.
Friedman, Richard C.; Downey, Jennifer I.
Archives of Sexual Behavior, 29, 6, 567
Dec, 2000
ISSN: 0004-0002 LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 9961 LINE COUNT: 00832

... be anyone--man, woman, or child, or even nonhuman--and all is possible. Freud termed the organization of the unconscious part of the mind the " **primary process** " and **contrasted** it with the " **secondary process** " --the system of organization of ordinary, everyday thinking (Freud, 1940). A perspective about fantasy, unique to psychoanalytic psychology, is that beneath the immediately coherent narrative...

4/3,K/12 (Item 6 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

11790345 SUPPLIER NUMBER: 58617879 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Comparison Effects on Preference Construction.
DHAR, RAVI; NOWLIS, STEPHEN M.; SHERMAN, STEVEN J.
Journal of Consumer Research, 26, 3, 293
Jan, 1999
ISSN: 0093-5301 LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 11902 LINE COUNT: 01039

TEXT:

...tested in a series of studies. One study, which focused on alternatives about which consumers have information in memory, shows that the direction of an **initial comparison task** that elicits differences between two options systematically alters their relative attractiveness in a **subsequent preference task**. In two subsequent studies, the effect of engaging in an **initial comparison task** on subsequent preference **judgments** was tested for stimulus-based choice sets. The results on choice deferral and choice satisfaction were consistent with the notion that engaging in similarity/dissimilarity...

... show that the direction of an initial comparison task that elicits differences between two options systematically alters the relative

attractiveness of these options in a **subsequent preference task**. The next set of studies focuses on the effect of engaging in an **initial comparison task** on subsequent choices and choice satisfaction for stimulus-based sets. The effect on preference and choice satisfaction are consistent with the notion that engaging in...the unique features of Paris). Because the alternatives in this case (Paris and Hawaii) have more positive than negative features, the focal alternative in the **initial comparison task** is likely to appear relatively more attractive in a **subsequent preference task** between the two alternatives.

In addition, the focus-shift framework makes an opposite prediction for relative preferences in the case where the two alternatives have...

4/3,K/13 (Item 7 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

09830774 SUPPLIER NUMBER: 17781363 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Comparison of simulated and experimental CD-limited yield for a submicron i-line process.
Charrier, Edward W.; Mack, Chris A.; Progler, Christopher J.
Solid State Technology, v38, n11, p105(5)
Nov, 1995
ISSN: 0038-111X LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 2776 LINE COUNT: 00264

... been developed. The "CD-limited yield metric" that is derived from this procedure can be used to characterize a process, but is only a relative **measure** of the actual CD-limited yield of the process.

There are several opportune future directions. **First, other process** parameters can be added to the simulation to produce a different simulated distribution. Investigation of these parameters can determine the sensitivity of the process to...

4/3,K/14 (Item 8 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

06291270 SUPPLIER NUMBER: 17745672 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Early employment and mobility behaviors of business graduates in the Arab Gulf Region: implications for multinational corporations. (Management and its Environment in the Arab World)
Abdel-Halim, Ahmed A.; Ashour, Ahmed S.
International Studies of Management & Organization, v25, n3, p67(20)
Fall, 1995
ISSN: 0020-8825 LANGUAGE: English RECORD TYPE: Fulltext; Abstract
WORD COUNT: 6625 LINE COUNT: 00563

... 2 percent moved to supervisory jobs ((Chi).sup.2) = 5.29, p (less than) 0.05). Men were worse off in terms of job-major **match** on their **second jobs** as compared with women. Only 29.2 percent of male job-shifters in mismatched **first jobs** moved to matched jobs, while 70.8 percent moved to mismatched jobs once more ((Chi).sup.2) = 20.82, p (less than) 0.001). The...

...shifters, the trend was for a somewhat consistent movement to nonsupervisory jobs. Kuwaiti job-shifters also improved their relative position with regard to job-major **match**. While only 21.1 percent of Kuwaiti job-shifters on **matched first jobs** moved to mismatched **second jobs**, 78.9 percent moved to **matched second jobs**; and 43.4 percent of those in mismatched **first jobs** made an improvement by moving to **matched jobs**, as compared with 56.6 percent continuing to be in mismatched positions ((Chi).sup.2) = 15.07, p (less than) 0.001). Even though the movement of non-Kuwaiti job-shifters from **matched first job** to **matched second jobs** was somewhat similar to Kuwaiti job-shifters (75.0 percent), a greater number of non-Kuwaitis who changed jobs continued in mismatched jobs the second...

4/3,K/15 (Item 9 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB
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08291260 SUPPLIER NUMBER: 17745606 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Optimal 'mismatch' and promotions.

Hersch, Joni

Economic Inquiry, v33, n4, p611(14)

Oct, 1995

ISSN: 0095-2583 LANGUAGE: English RECORD TYPE: Fulltext; Abstract

WORD COUNT: 6584 LINE COUNT: 00593

... 50)

Age	41.48 (7.08)
Married	.75 (.43)
Hourly Wage	14.94 (3.28)
Weeks of Formal on-the-Job	4.32
Training for First Job (TRAINING)	(8.64)
Number of Promotions (PROMOTIONS)	1.29 (1.57)
Number of Different Jobs with Employer	4.03 (2.53)
Job Match for First Job with Current Employer Mean and Std. Dev.	
Under-Qualified for First Job	.13 (.34)
Exactly Qualified for First Job	.58 (.49)
Over-Qualified for First...	

4/3,K/16 (Item 10 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2004 The Gale Group. All rts. reserv.

05887897 SUPPLIER NUMBER: 12255351 (USE FORMAT 7 OR 9 FOR FULL TEXT)

How much do we really know about moonlighters?

Baba, Vishwanath V.; Jamal, Muhammad

Public Personnel Management, v21, n1, p65(9)

Spring, 1992

ISSN: 0091-0260 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

WORD COUNT: 3069 LINE COUNT: 00260

... headaches, insomnia, loss of appetite, etc.), marital happiness and job stress (work overload, anxiety, pressure, etc.). In general, moonlighters experienced higher job satisfaction in their **primary jobs** that did nonmoonlighters. No study attempted to **assess** the job satisfaction of moonlighters in their **second job**. [Tabular Data 2 Omitted]

Moonlighters were more active in social participation than nonmoonlighters. They belonged to more voluntary organizations, attended more meetings, and spent more...

4/3,K/17 (Item 11 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB
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02167433 SUPPLIER NUMBER: 03375500 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Big headaches come in small parcels.

Trunick, Perry A.

Handling & Shipping Management, v25, p38(4)

Aug, 1984

ISSN: 0194-603X
WORD COUNT: 2434

LANGUAGE: ENGLISH
LINE COUNT: 00187

RECORD TYPE: FULLTEXT

... you have a high turnover a your shipping room or processing parcels is an added duty that is shared by a number of workers doing **other primary tasks**, this could be an important feature.

Look for a "**comparison**" key or process that allows you to have the computer in the parcel manifesting system shop for the lowest rate. Orbitran and other manufacturers allow...

4/3,K/18 (Item 1 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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02686157 439780541

The enterprise map: A system for implementing strategy and achieving operational excellence

Burton, Herbert O; Pennotti, Michael C
Engineering Management Journal v15n3 PP: 15-20 Sep 2003
ISSN: 1042-9247 JRNL CODE: EGM
WORD COUNT: 4570

...TEXT: need. The first transforms parts from suppliers into products that are offered for sale. These products are inputs to the second process. The second process **matches** customer needs with available products and completes a sale whenever a **match** exists. The **second process** also generates customer information that is used by the **first process** to guide product development.

The enterprise map shown in Exhibit 1 illustrates this simple, two-process example. The diagram identifies the external interfaces of the...

4/3,K/19 (Item 2 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2004 ProQuest Info&Learning. All rts. reserv.

02620917 372797031

Employee expectations and motivation: An application from the "learned helplessness" paradigm

Schepman, Steven B; Richmond, Lynn
Journal of American Academy of Business, Cambridge v3n1/2 PP: 405 Sep 2003
JRNL CODE: JAAB
WORD COUNT: 3087

...ABSTRACT: motivation theory of Victor Vroom. Perceptions of levels of helplessness were manipulated in an experimental design using random, non-contingent feedback and failure on an **initial task**. Subjects' level of perceived ability and control on a **second task** were **assessed** prior to beginning the subsequent task. Statistically significant differences were found between the levels of helplessness groups and the control group on perceptions of control...

...TEXT: motivation theory of Victor Vroom. Perceptions of levels of helplessness were manipulated in an experimental design using random, non-contingent feedback and failure on an **initial task**. Subjects' level of perceived ability and control on a **second task** were **assessed** prior to beginning the subsequent task. Statistically significant differences were found between the levels of helplessness groups and the control group on perceptions of control...

4/3,K/20 (Item 3 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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02618943 356902901

Integrating errors into the training process: The function of error management instructions and the role of goal orientation

Heimbeck, Doerte; Frese, Michael; Sonnentang, Sabine; Keith, Nina
Personnel Psychology v56n2 PP: 333 Summer 2003
ISSN: 0031-5826 JRNL CODE: PPS
WORD COUNT: 10777

...TEXT: variables (Figure 1). The [eta]

sup 2

values were medium to high according to Cohen (1988). Cohen's d values ranged from 0.63 (for **second phase tasks** of medium difficulty) to 0.90 (for **first phase tasks** of medium difficulty), **comparing** error training with error management instructions with error avoidant training.

Figure 1: Means and Standard Errors of Performance Variables in Three Training Conditions Adjusted for...

4/3,K/21 (Item 4 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

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02540640 284053311

The concept of knowledge and how to measure it

Hunt, Darwin P

Journal of Intellectual Capital v4n1 PP: 100-113 2003

ISSN: 1469-1930 JRNL CODE: NTTL

WORD COUNT: 6248

...TEXT: correctness of their own responses. The surprising results of some of our first studies (Hunt, 1982; Sams, 1989) indicate that learning is expedited by self- **assessment** (SA) responding. Our expected result was that the **secondary task** of **assessing** the correctness of one's responses would interfere with the **primary task** of learning.

Some insight into how to interpret the observation that SA responding enhances learning is provided by a signal detection analysis (Green and Swets...

4/3,K/22 (Item 5 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

(c) 2004 ProQuest Info&Learning. All rts. reserv.

02533050 115924280

Performance measurement system design: developing and testing a process-based approach

Neely, Andy; Mills, John; Platts, Ken; Richards, Huw; Gregory, Mike; Bourne, Mike; Kennerley, Mike

International Journal of Operations & Production Management v20n10 PP: 1119-1145 2000

ISSN: 0144-3577 JRNL CODE: IJO

WORD COUNT: 8379

...TEXT: system design be specified and, if so;

what would that process involve?

Addressing these questions involved the authors in six phases of research. The **first - process** design - required the authors to specify a process for performance **measurement** system design. The **second - process** development - involved enhancing this process through participatory action research studies in three UK manufacturing business units. The third - process documentation - involved documenting the revised process...

4/3,K/23 (Item 6 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

(c) 2004 ProQuest Info&Learning. All rts. reserv.

02372886 116354204

The effects of individual time urgency on group polychronicity

Waller, Mary J; Giambattista, Robert C; Zellmer-Bruhn, Mary E

Journal of Managerial Psychology v14n3/4 PP: 244-256 1999

ISSN: 0268-3946 JRNL CODE: JMN

WORD COUNT: 4930

...TEXT: is the average of these focus proportions across all 4 percent intervals and therefore represents the degree to which the group was focused on its **primary task** at a given time relative to **other tasks**

A second **measure**, DEVMONO, identified "deviations from monochronicity," which was captured by identifying that subset of phase transitions which either skipped the next logical phase of the problem...

4/3,K/24 (Item 7 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

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0416332 86064726

The service concepts in practice: compromises in high street trading

T. Alistair J. Nicholson

Managing Service Quality v6n5 PP: 53-56 1996

ISSN: 0960-4529 JRNL CODE: MAQ

WORD COUNT: 1976

...TEXT: the design, running and evaluation of high street systems. In this exercise four basic questions are proposed, each of which provide a perspective of conspicuous **contrasts**. The **first task** is to **match** the marketing proposition of the store to the internal organization image. The **second task** is to examine the inside of the store viewed "outwards" to see how the provision of the service supports the variety of customers and their...

4/3,K/25 (Item 8 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

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02275810 86922734

Social-dynamical aspects of quality management in NPD

Fisscher, Olaf A M; De Weerd-Nederhof, Petra C

TQM Magazine v12n6 PP: 408 2000

ISSN: 0954-478X JRNL CODE: TQM

WORD COUNT: 9338

...TEXT: performances, such as accomplished functions, safety, ease of use, and so on. This is a consumer-side conceptualization of quality: quality as the consumers' **evaluation** of products' fitness for use. Concerning the interactions of NPD with the **other primary processes** we have to look at its contribution to internal quality (IQ), which is aimed at internal efficiency (manufacturability, serviceability, etc.). NPD can improve efficiency, meaning...

4/3,K/26 (Item 9 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

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02187097 74444014

The power of process management

McNeese, William; Marks, Carol

Quality Congress. Asq... Annual Quality Congress Proceedings PP:
300-310 2001

JRNL CODE: QUCO
WORD COUNT: 3826

...TEXT: processes listed by the International Benchmarking Clearinghouse and Arthur Andersen. The purpose of this list is to serve as a checklist against which companies can **compare** their own processes to ensure that none were overlooked. Each **primary process** contained numerous **secondary processes**. Some of these processes were combined for distribution companies in the 1995 Facing the Forces of Change: Transforming Your Business with Best Practices report published...

4/3,K/27 (Item 10 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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01558792 02-09781

Earning patterns and changes in economics and other sciences

O'Neill, June; Sicherman, Nachum
Eastern Economic Journal v23n4 PP: 395-409 Fall 1997
ISSN: 0094-5056 JRNL CODE: EEJ
WORD COUNT: 4470

...TEXT: entirely eliminate possible cohort effects.

The earnings information that is available consistently for all survey years is the annual salary rate on the respondent's **primary job**. This **measure** excludes earnings from **second jobs** and supplementary sources such as summer jobs, consulting, or bonuses.⁵ In some years respondents were asked to report the salary on the primary job...

4/3,K/28 (Item 11 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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01441292 00-92279

Job patterns of disabled beneficiaries

Hennessey, John C
Social Security Bulletin v59n4 PP: 3-11 Winter 1996
ISSN: 0037-7910 JRNL CODE: SSB
WORD COUNT: 4873

...TEXT: Subsequent Jobs

Chart 9 shows that 56 percent of the DI beneficiaries who start a second job end the job before retiring, recovering, or starting **another job**, as **compared** with the 50 percent who ended the **first job** (chart 3). For those who start a **second job**, only 10 percent make their next transition to a recovery termination. This percentage is less than half of the **comparable** 24 percent who recover next after the start of the **first job** (chart 3). Clearly, **second jobs** are less likely to be successful in terms of continued work and/or program outcomes than the first job. For those who start a second...

4/3,K/29 (Item 12 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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01378720 00-29707

Flowmeter selection isn't easy, but tools are here

Blickley, George J
Control Engineering v43n15 PP: 43-50 Nov 1996
ISSN: 0010-8049 JRNL CODE: STCE

...ABSTRACT: record-keeping. Regardless of why a user wants to measure flow, the challenge is to select the most applicable technology. The number of ways to measure flow is right up there with all the other primary process variables of pressure, temperature, and level. Over the years, many ingenious and enterprising methods have been brought to the market, some falling by the wayside...

4/3,K/30 (Item 13 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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01004660 96-54053

Monitoring process parameters with pre-control

Olorunniwo, Festus; Knight, John Ed

Journal of Operations Management v12n2 PP: 75-87 Feb 1995

ISSN: 0272-6963 JRNL CODE: JOT

ABSTRACT: Two models are proposed for evaluating process parameters without the need to record any numerical measurement of product characteristics. Although the 2 models are jointly executed, the first evaluates process variability, and the second process mean. Common pre-control (PC) procedure normally uses indicator gages with zones colored identically as the zones of the "charts." It is shown that the...

4/3,K/31 (Item 14 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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00996487 96-45880

Rose's recovery - Built to last

Corwin, Pat

Discount Merchandiser v35n3 PP: 26-28+ Mar 1995

ISSN: 0012-3579 JRNL CODE: DMD

WORD COUNT: 2019

...TEXT: that he is "a consensus-builder, a team-builder," who was able to help organize a complex plan and get it approved. "In December, the judge approved it. After the first job --getting the plan done--came the second job. The plan required the company to pay down the secured debt--which we have done. The secured lenders have been paid down to about \$26...

4/3,K/32 (Item 15 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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00727938 93-77159

Matchmaker, Matchmaker: The Effect of Old Boy Networks on Job Match Quality, Earnings, and Tenure

Simon, Curtis J.; Warner, John T.

Journal of Labor Economics v10n3 PP: 306-330 Jul 1992

ISSN: 0734-306X JRNL CODE: JLB

WORD COUNT: 7637

...TEXT: job match procedures can be used to infer the quality of information possessed by employers prior to hire.

Definitions and the relative frequencies of job match procedure appear in table 1 for three subsamples: all jobs, first jobs, and subsequent jobs. (Table 1 omitted) Focusing first on all jobs (col. 1), RECRUIT matches were most frequent (21.3%), followed by INSIDER (19.9%), COLLEGE PLACEMENT (13.0%), WANT AD (12.5%), and OTHER (10.9%) matches. A considerably... office after their first job will probably be perceived by employers to be of lower productivity. There is a large positive premium for COLLEGE PLACEMENT matches on first jobs (col. 2) but a sizeable

negative premium on subsequent jobs (col. 3). These results also indicate that workers on their first job who resort to non-COLLEGE PLACEMENT channels are perceived by employers to be of lower productivity. (14)

The number of jobs held since the start... particularly productive individual.

14 It is not surprising that the positive effect of COLLEGE PLACEMENT dominates in the subsample of all jobs because few such matches occur in subsequent jobs.

15 We examined how the existence of first job information was correlated with the wage in 1972 by creating a dummy variable, FJINFO, equal to one if there was first job information for the...

4/3,K/33 (Item 16 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2004 ProQuest Info&Learning. All rts. reserv.

00725644 93-74865
Panel Estimates of Males and Female Job Turnover Behavior: Can Female Nonquitters Be Identified?
Light, Audrey; Ureta, Manuelita
Journal of Labor Economics v10n2 PP: 156-181 Apr 1992
ISSN: 0734-306X JRNL CODE: JLB
WORD COUNT: 8983

...TEXT: length as a covariate. Future job length was measured as the length of the second observed job and, alternatively, as the average length of all subsequent jobs. If the hazard of job separation for the first jobs is independent of the length of subsequent jobs, then we can infer that match quality is the source of unobserved heterogeneity (see Flinn and Heckman 1982). In contrast, a significant coefficient on the length of future jobs would suggest...

4/3,K/34 (Item 17 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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00476750 90-02507
An Interactive Simulator for Statistical Process Control
Chandra, M. Jeya; Melloy, Brian J.
Computers & Industrial Engineering v17n1-4 PP: 186-190 1989
ISSN: 0360-8352 JRNL CODE: CIE

...ABSTRACT: several representative industrial processes. The program generates product quality characteristic values that are concurrently monitored by standard control charting methods. It requires users to specify initial process parameter values and subsequent process adjustments. The effectiveness of these decisions is measured by economic criteria. Use of the software encourages a hands-on approach, which better prepares students to make quality improvements in an industrial environment through...

4/3,K/35 (Item 18 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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00237496 84-16057
Info Resource Management Aids Data Security
Liggett, Rosy
Computerworld v18n18 PP: Special Report 49-50 Apr 30, 1984
ISSN: 0010-4841 JRNL CODE: COW

...ABSTRACT: an attempt to address the problem of security. Defining the data is the first step in IRM; then, the value of the data must be assessed. Limiting exposure is the first task of a security program. Providing auditability is the other crucial task in a security program. Methods to limit unauthorized access include: 1. encryption, 2. passwords, 3. software partitioning, and 4. callback.

4/3,K/36 (Item 19 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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00153161 81-23038

Indicators of Social Change: Developments in the United States of America

Parke, Robert; Peterson, James L.

Accounting, Organizations & Society v6n3 PP: 235-246 1981

ISSN: 0361-3682 JRNL CODE: AOS

...ABSTRACT: is the development of comprehensive schemes for classifying and ordering social data to assist interpretation. Social reporting is the dissemination of the results of social measurement. The first task before social indicators researchers is the improvement of measurement techniques. The second task is the selection and institutionalization of indicators. Yet another possible direction for social indicators research to take is in efforts to forecast social trends. Forecasting...

File 8: Ei Compendex(R) 1970-2004/Feb W4
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File 35: Dissertation Abs Online 1861-2004/Feb
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File 144: Pascal 1973-2004/Feb W4
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File 434: SciSearch(R) Cited Ref Sci 1974-1989/Dec
(c) 1998 Inst for Sci Info
File 34: SciSearch(R) Cited Ref Sci 1990-2004/Feb W5
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File 438: Library Lit. & Info. Science 1984-2004/Feb
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Set	Items	Description
S1	23827	(1ST OR FIRST OR PRIMARY OR INITIAL OR PARENT OR ROOT) (1W) - (PROCESS OR PROCESSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S2	90483	(2ND OR SECOND??? OR CHILD OR SUBSEQUENT OR FOLLOWING OR E- NSUING OR OTHER OR ANOTHER OR DIFFERENT) (1W) (PROCESS OR PROCE- SSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S3	161	(COMPAR? OR CONTRAST? OR WEIGH? OR JUDG? OR MATCH??? OR EV- ALUAT? OR ASSESS? OR MEASUR?) (7N) S1(7N) S2
S4	125	RD (unique items)
S5	99	S4 NOT PY=2000:2004
S6	0	S5 AND THREAD? ?
S7	77	S5 AND (TASK? ? OR JOB? ?)
S8	38	S5 AND (PROCESS OR PROCESSES)
S9	61	S5 NOT S8

8/5/4 (Item 4 from file: 8)
DIALOG(R)File 8:EI Compendex(R)
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00951217 E.I. Monthly No: EI8009066667 E.I. Yearly No: EI80027363
Title: **SPEED MEASURE AND SPEED CONTROL WITH A MULTI-MICROPROCESSORS
SYSTEM ON dc MOTORS.**

Author: Demerle, M.; Fromont, J.
Corporate Source: Ec Super d'Electr, Gif-sur-Yvette, Fr
Source: IECI Annu Conf Proc 6th, Pap Presented at IEC'80 Appl of Mini and
Microcomput, Philadelphia, Pa, Mar 17-20 1980. Publ by IEEE (Cat n
80CH1551-1), Piscataway, NJ, 1980 p 40-44
Publication Year: 1980
CODEN: IACPDC ISSN: 0271-8308
Language: ENGLISH
Journal Announcement: 8009

Abstract: A description is given of a digital speed control for high
power dc motors. This high performances feedback control is based on a
multi-processors structure which splits up the **different tasks**. A
first MPU **processes** the pulse generator with an original method which
provides a speed **measure** with a constant accuracy. Another processor
controls the speed according to a proportional integral algorithm. The data
concerning the actual state of the **process** and the operator instructions
are acquired thru another MPU while a last one manages the I/O exchanges
(peripherals or central computers). All those processors share the datas by
means of a common memory. 2 refs.

Descriptors: *ELECTRIC MOTORS, DC--*Control Systems; CONTROL SYSTEMS--
Computer Applications; COMPUTERS, MICROPROCESSOR

Classification Codes:
705 (Electric Generators & Motors); 731 (Automatic Control Principles);
723 (Computer Software); 722 (Computer Hardware)
70 (ELECTRICAL ENGINEERING); 73 (CONTROL ENGINEERING); 72 (COMPUTERS &
DATA PROCESSING)

8/5/6 (Item 2 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online
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01375188 ORDER NO: AAD94-24467
ENTROPY-BASED EVALUATION OF ADAPTIVELY CONSTRUCTED NEURAL NETWORKS

Author: ADELSBERGER-MANGAN, DAWN MARIE
Degree: PH.D.
Year: 1993
Corporate Source/Institution: UNIVERSITY OF VIRGINIA (0246)
Adviser: WILLIAM B. LEVY
Source: VOLUME 55/05-B OF DISSERTATION ABSTRACTS INTERNATIONAL.
PAGE 1930. 162 PAGES
Descriptors: ENGINEERING, BIOMEDICAL; ENGINEERING, ELECTRONICS AND
ELECTRICAL; ARTIFICIAL INTELLIGENCE
Descriptor Codes: 0541; 0544; 0800

This research explores the role of adaptive **processes** in
constructing networks which perform successful transformations on their
inputs. Successful transformations are characterized by the minimization of
two information measures: the information loss incurred with the
transformation and the output statistical dependence. However, because the
networks are computational models of the dentate gyrus region of the
hippocampus, they must reflect the anatomy and physiology of the region.
The networks are composed of binary neurons, connected by excitatory,
feed-forward synapses. Synaptic strengths are adjusted using an expression
inspired by long-term potentiation and depression.

The first simulations explored the interaction of firing threshold,
synaptic connectivity, and input environment statistical dependence, on the
ability of networks with fixed connectivity to create successful
transformations. Additionally, the utility of two adaptive **process** in
facilitating successful network transformation was explored. The **first**
process, associative **weight** modification, adjusted the **weights** of the

synaptic connections. The **second process**, firing threshold adjustment, modified the firing threshold of the output neurons. The results of these simulations illustrated the importance of maintaining appropriate levels of output firing in creating successful transformations.

The next simulations eliminated the difficulties encountered with fixed synaptic connectivity, and incorporated the success of networks where the output neurons fired 50% of the time. In these simulations a synaptic connectivity was built that insured that each output neuron fired approximately 50% of the time. The synaptic connectivity was constructed using two **processes**: synaptogenesis, which created new synaptic connections; and associative synaptic modification, which adjusted the weight of existing synapses. Synaptogenesis produced additional innervation for each output neuron until it fired approximately 50% of the time. Associative modification of synapses lent robustness to network construction by adjusting suboptimal choices of initial synaptic weight. Networks constructed using these two mechanisms preserved the information content of a variety of inputs over a range of firing thresholds.

Next, two additional mechanisms were incorporated into network construction. The first **process**, input neuron avidity, regulated the tendency of an input neuron to participate in the construction of new synapses. The second **process**, selective synapse removal, eliminated synapses between neurons with uncorrelated firing. The simulations demonstrated that incorporating selective synapse removal, when combined with a mechanism which limited the tendency of the input neurons to participate in synaptogenesis, resulted in increased output representational information and reduced output statistical dependence as compared to networks where synapses were not removed and the ability of the input neurons to create new synapses was not limited.

This research details a robust, "hands-off", methodology for constructing biologically plausible network architectures which are able to successfully transform inputs. Additionally, the networks are constructed using only information that is present locally at the neurons and synapses. (Abstract shortened by UMI.)

8/5/14 (Item 2 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2004 Institution of Electrical Engineers. All rts. reserv.

5952579 INSPEC Abstract Number: C9808-0310F-021

Title: Descriptive modeling of software processes

Author(s): Becker, U.; Hamann, D.; Verlage, M.

Author Affiliation: Fraunhofer Inst. for Exp. Software Eng., Kaiserslautern, Germany

Conference Title: SPI 97. 'How to Improve: Practice and Experience'. The European Conference on Software Process Improvement. Conference Proceedings

Publisher: SPI 97 Office, Farnham, UK

Publication Date: 1997 Country of Publication: UK 474 pp.

Material Identity Number: XX97-02923

Conference Title: Proceedings of Meeting on Software Process Improvement. SPI 97

Conference Date: 1-4 Dec. 1997 Conference Location: Barcelona, Spain

Availability: SPI 97 Office, Meeting Management, The Chestnuts, 1st Floor, 18 East Street, Farnham, Surrey GU9 7SD, UK

Language: English Document Type: Conference Paper (PA)

Treatment: Applications (A); Practical (P)

Abstract: An approach to descriptive **process** modeling is described. A description of the **process**, a **process** model, is essential for successful **process** improvement. **First**, a **process** model serves as a baseline for **process** change. **Secondly**, the **process** model is needed to identify **measurement** points. Furthermore, an explicit representation of the **process** supports **process** understanding. Capturing an accurate model of the actual **process** in a software development organization is thus one key factor of systematic **process** management. Not enough experience exists on how to model **processes** descriptively. An 8-step approach to descriptive **process** modeling is described which is based on practical experience. The approach consists of two phases: the set-up phase and the

execution phase. The approach has been tried in several **process** improvement programs undertaken in large software development organizations. Although still tentative, the approach could be used to collect valuable experience about important issues for the **process** modeler. The performance of the eight steps is illustrated by industrial modeling cases. (21 Refs)

File 347:JAPIO Oct 1976-2003/Oct(Updated 040202)

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File 350:Derwent WPIX 1963-2004/UD,UM &UP=200415

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Set	Items	Description
S1	14717	(1ST OR FIRST OR PRIMARY OR INITIAL OR PARENT OR ROOT) (1W) - (PROCESS OR PROCESSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S2	39472	(2ND OR SECOND??? OR CHILD OR SUBSEQUENT OR FOLLOWING OR E- NSUING OR OTHER OR ANOTHER OR DIFFERENT) (1W) (PROCESS OR PROCE- SSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S3	171	(COMPAR? OR CONTRAST? OR WEIGH? OR JUDG? OR MATCH??? OR EV- ALUAT? OR ASSESS? OR MEASUR?) (7N) S1 (7N) S2
S4	18	S3 AND THREAD? ?
S5	153	S3 NOT S4
S6	35	S5 AND IC=G06F

4/5/3 (Item 3 from File: 347)

DIALOG(R) File 347:JAPIO

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06178458 **Image available**

METHOD AND DEVICE FOR PROCESS CONTROL OF SYSTEM MANAGEMENT

PUB. NO.: 11-120007 [JP 11120007 A]

PUBLISHED: April 30, 1999 (19990430)

INVENTOR(s): NAGAMATSU OSAMU
ARIMA KEIKO

APPLICANT(s): NEC CORP

APPL. NO.: 09-285685 [JP 97285685]

FILED: October 17, 1997 (19971017)

INTL CLASS: G06F-009/46; G06F-009/46; G06F-011/30

ABSTRACT

PROBLEM TO BE SOLVED: To easily recognize which process has become abnormal by defining whether or not there is a start response or stop response and a start limit time and performing multi- **thread** start/stop control processing.

SOLUTION: A start/stop request part 23 of system management 2 generates two kinds of **threads** which are a start/stop limit time checking **thread** 24 as a **1st** checking **thread** and a time interval checking **thread** 25 as a **2nd** checking **thread**. The **1st** checking **thread** 24 is made into multiple **threads**, start/stop elapsed times are **measured** by processes at the same time, and necessary start/ stop times are compared with a maximum time to decide whether or not they exceed the maximum time, so that the name of an exceeding process is outputted. Further, the 2nd checking **thread** 25 is summarized by processes, the set maximum time interval between the processes is compared with the time intervals of continuous actuation of the processes, and the name of a decided process having exceeded the maximum time interval is outputted.

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4/5/13 (Item 9 from File: 350)

DIALOG(R) File 350:Derwent WPIX

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011313546 **Image available**

WPI Acc No: 1997-291450/199727

XRPX Acc No: N97-241169

**Profiling process having number of threads executing on computer system
- uses dynamic instrumentation to instruct threads to calculate and
store performance metric**

Patent Assignee: HEWLETT-PACKARD CO (HEWP)

Inventor: SUMMERS C

Number of Countries: 005 Number of Patents: 007

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 777181	A1	19970604	EP 96308586	A	19961127	199727 B
JP 9237203	A	19970909	JP 96332752	A	19961128	199746
US 5838976	A	19981117	US 95563334	A	19951128	199902
EP 777181	B1	20010822	EP 96308586	A	19961127	200149
DE 69614645	E	20010927	DE 614645	A	19961127	200164
			EP 96308586	A	19961127	
US 6401240	B1	20020604	US 95563334	A	19951128	200242
			US 98130761	A	19980807	
JP 3337926	B2	20021028	JP 96332752	A	19961128	200278
Priority Applications (No Type Date): US 95563334 A 19951128; US 98130761 A 19980807						

Cited Patents: 2.Jnl.Ref; EP 422945; US 5265249

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 777181	A1	E	13	G06F-011/34	
Designated States (Regional): DE FR GB					
JP 9237203	A		10	G06F-011/28	
US 5838976	A			G06F-009/45	
EP 777181	B1	E		G06F-011/34	
Designated States (Regional): DE FR GB					
DE 69614645	E			G06F-011/34	Based on patent EP 777181
US 6401240	B1			G06F-009/45	Cont of application US 95563334
					Cont of patent US 5838976
JP 3337926	B2		12	G06F-011/34	Previous Publ. patent JP 9237203

Abstract (Basic): EP 777181 A

The profiler measures performance metrics for all **threads** executing on a symmetric multiprocessor (SMP) computer system. The profiler uses dynamic instrumentation to cause **threads** to sample performance metrics before and after certain code regions, and uses extensions to parallel support layer to register a **parent thread** with its **child threads**. Each **thread** stores the **measured** performance metric, or delta, in memory cells corresponding to its region and its parent region.

When the process is complete, the profiler scans through the memory storage areas and sums the deltas for each particular level of code. The results may be analysed at the **thread** or process level, and the profiler can then be adapted to work with any process executing on the computer system.

USE - Profiling code on symmetric multiprocessor architectures.

ADVANTAGE - Enables profiling of code on symmetric multiprocessor systems, that accounts for all performance metrics within code region on **thread-by-thread** level. Provides summation of performance metrics for entire process after single run of process.

Dwg.1/6

Title Terms: PROFILE; PROCESS; NUMBER; **THREAD** ; EXECUTE; COMPUTER; SYSTEM; DYNAMIC; INSTRUMENT; INSTRUCTION; **THREAD** ; CALCULATE; STORAGE; PERFORMANCE; METRIC

Derwent Class: T01

International Patent Class (Main): G06F-009/45; G06F-011/28; G06F-011/34

File Segment: EPI

6/5/30 (Item 14 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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011518316 **Image available**
WPI Acc No: 1997-494802/199746
XRPX Acc No: N97-411966

Design improvement system for logic circuit e.g. multiplier in large scale integrated circuit - has circuit production unit which produces improved version of first logic circuit by connecting cell symbols based on second net list selected by circuit recognition selection unit

Patent Assignee: FUJITSU LTD (FUJIT)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 9231258	A	19970905	JP 9641820	A	19960228	199746 B

Priority Applications (No Type Date): JP 9641820 A 19960228

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 9231258	A	11	G06F-017/50	

Abstract (Basic): JP 9231258 A

The system includes a test pattern comparison detector (12) which detects a second test pattern (D2) stored in a first table (5) that corresponds with a first test pattern (TP) from the simulation result (18) of a first logic circuit (17). Based on the second test pattern, a simulation executing unit (13) simulates the first logic circuit. From the simulation result of the simulation executing unit, a simulation comparison detector (13a) detects a third test pattern (D4) corresponding to the first test pattern. Based on the third test pattern, a net list comparison detector (14) detects a second net list corresponding to a first net list (NR) from the first logic circuit.

From the second net list, a process order **comparison** detector (15) detects a **second process** order and **compares** it with a **first process** order (SD) detected by a process order detector (3). Based on the comparison result, a circuit recognition selection unit (6) selects a second net list from the detected net list. Based on the selected second net list, a circuit production unit (10) connects the cell symbols to produce an improved version of the first logic circuit.

ADVANTAGE - Enables automatic modification of logic circuit.

Dwg.1/8

Title Terms: DESIGN; IMPROVE; SYSTEM; LOGIC; CIRCUIT; MULTIPLIER; SCALE; INTEGRATE; CIRCUIT; CIRCUIT; PRODUCE; UNIT; PRODUCE; IMPROVE; VERSION; FIRST; LOGIC; CIRCUIT; CONNECT; CELL; SYMBOL; BASED; SECOND; NET; LIST; SELECT; CIRCUIT; RECOGNISE; SELECT; UNIT

Index Terms/Additional Words: LSI

Derwent Class: T01

International Patent Class (Main): G06F-017/50

File Segment: EPI

6/5/31 (Item 15 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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011239387 **Image available**
WPI Acc No: 1997-217290/199720
XRPX Acc No: N97-179274

Test result collation method for various processes in data transmission system - involves comparing first process and sequence tables with second process and sequence table

Patent Assignee: FUJITSU LTD (FUJIT)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 9062531	A	19970307	JP 95219985	A	19950829	199720 B

Priority Applications (No Type Date): JP 95219985 A 19950229

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes
JP 9062531 A 6 G06F-011/22

Abstract (Basic): JP 9062531 A

The method involves storing the expected operation data for different combination of processes that carries out communication in a first process table (1). The communication data between the process, according to the expected operation sequence is stored in a first sequence table (2). Based on the expected operation data, the communication between the processes is carried out. Then the data obtained by the result of communication operation are collected.

The communication operation result data are stored in a second process table for various combination of the processes. The communication data obtained according to the communication in the area of desired combination is stored in a record second sequence table (4). Then the **first process** and sequence tables are **compared** with the **second process** and sequence table.

ADVANTAGE - Simplifies test result collation processing. Enables identification of normality of real time processing system.

Dwg.1/5

Title Terms: TEST; RESULT; COLLATE; METHOD; VARIOUS; PROCESS; DATA; TRANSMISSION; SYSTEM; COMPARE; FIRST; PROCESS; SEQUENCE; TABLE; SECOND; PROCESS; SEQUENCE; TABLE

Derwent Class: T01

International Patent Class (Main): G06F-011/22

International Patent Class (Additional): G06F-009/46

File Segment: EPI

6/5/32 (Item 16 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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010941419 **Image available**

WPI Acc No: 1996-438369/199644

XRPX Acc No: N96-369426

Printing operation controlling method for printer - involves storage of binary data compressed by second process when compression efficiency of second process is greater than first process

Patent Assignee: CANON KK (CANO)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 8216462	A	19960827	JP 9521596	A	19950209	199644 B

Priority Applications (No Type Date): JP 9521596 A 19950209

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes
JP 8216462 A 6 B41J-005/30

Abstract (Basic): JP 8216462 A

The method involves performing a first process by which a stored binary data is compressed. A second process further compresses the compressed binary data. The compression efficiency of first and **second process** is **compared**. When the compression efficiency of the **second process** is higher than the **first process**, the binary data compressed by **second process** is stored.

The whole procedure is repeated with compressed data of second process as stored binary data.

ADVANTAGE - Uses printer memory efficiency.

Dwg.4/5

Title Terms: PRINT; OPERATE; CONTROL; METHOD; PRINT; STORAGE; BINARY; DATA; COMPRESS; SECOND; PROCESS; COMPRESS; EFFICIENCY; SECOND; PROCESS; GREATER; FIRST; PROCESS

Derwent Class: P75; T01; W02

International Patent Class (Main): B41J-005/30

International Patent Classification (Additional): G06F-003/12 ; G06T-009/00;
H04N-001/41
File Segment: EPI; EngPI

6/5/33 (Item 17 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2004 Thomson Derwent. All rts. reserv.

010170494 **Image available**
WPI Acc No: 1995-071747/199510
Related WPI Acc No: 1994-352467; 1995-071748; 1995-210359; 1996-347509;
2004-038039
XRPX Acc No: N95-056544

**Alignment method for wafer exposure system - computes relative position
with substrate at rotary position and controls mobility of substrate and
matches them to transfer position**

Patent Assignee: NIKON CORP (NIKR)
Inventor: FURUKAWA O; KAWAKUBO M; MAGOME N; TATENO H; YASUDA M
Number of Countries: 002 Number of Patents: 005
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 6349705	A	19941222	JP 93137642	A	19930608	199510 B
US 6278957	B1	20010821	US 94183879	A	19940121	200150
			US 94254524	A	19940606	
			US 94326952	A	19941021	
			US 95569400	A	19951208	
			US 99240599	A	19990201	
US 20010049589	A1	20011206	US 99240599	A	19990201	200203
			US 2001901708	A	20010711	
JP 3289264	B2	20020604	JP 93137642	A	19930608	200240
US 20030158701	A1	20030821	US 94183879	A	19940121	200356
			US 94254524	A	19940606	
			US 94326952	A	19941021	
			US 95569400	A	19951208	
			US 99240599	A	19990201	
			US 2001901708	A	20010711	
			US 2002299819	A	20021120	

Priority Applications (No Type Date): JP 93137642 A 19930608; JP 938194 A
19930121; JP 93137913 A 19930608; JP 93140580 A 19930611; JP 93263241 A
19931021; JP 94304525 A 19941208

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 6349705	A		19	H01L-021/027	
US 6278957	B1			G06F-015/00	CIP of application US 94183879 CIP of application US 94254524 CIP of application US 94326952 Cont of application US 95569400
US 20010049589	A1			G01C-009/00	Div ex application US 99240599
JP 3289264	B2		21	H01L-021/027	Previous Publ. patent JP 6349705
US 20030158701	A1			G06F-017/18	CIP of application US 94183879 CIP of application US 94254524 CIP of application US 94326952 Cont of application US 95569400 Div ex application US 99240599 Cont of application US 2001901708 Div ex patent US 6278957

Abstract (Basic): JP 6349705 A

The position matching method uses multiple domains (27-n) bearing the projection of the chip pattern. Each domain has alignment marks (29-n, 30-n, 34-n, 35-n) and a standard point (28-n). These are formed on wafer (8) and the co-ordinate values of alignment marks are measured. The mobile position of the substrate is controlled according to co-ordinate positions and the position matching of the domains to the transfer position is carried out.

The first process measures co-ordinates for selected position

of the substrate that is stationary. The **second process** obtains the conversion parameter for computing the co-ordinate position relative to the standard position with the mask of substrate at same rotary angle. Third process rectifies relative shape error of the pattern by controlling the mobile position of substrate and matches domain to transfer position.

ADVANTAGE - Reduces error of registration of pattern. Stops influence of expansion and contraction of chip pattern. Improves productivity of chip on semiconductor element.

Dwg.4/11

Title Terms: ALIGN; METHOD; WAFER; EXPOSE; SYSTEM; COMPUTATION; RELATIVE; POSITION; SUBSTRATE; ROTATING; POSITION; CONTROL; MOBILE; SUBSTRATE; MATCH; TRANSFER; POSITION

Derwent Class: P84; S02; U11

International Patent Class (Main): G01C-009/00; **G06F-015/00** ; **G06F-017/18** ; H01L-021/027

International Patent Class (Additional): G01C-017/00; G01C-019/00; G03F-009/00; G05D-003/12

File Segment: EPI; EngPI

6/5/34 (Item 18 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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009684941 **Image available**

WPI Acc No: 1993-378495/199348

XRPX Acc No: N93-292297

Line data provision method using conversion of measured parameter values - using storage of coded measured values in matrix and evaluation of matrix points exhibiting defined correlation to determine object point coordinates

Patent Assignee: EDS ELECTRONIC DATA SYSTEMS DEUT GMBH (EDSE-N); WISCHNIK A (WISC-I); EDS ELECTRONIC DATA SYSTEMS GMBH (EDSE-N); SPEIDEL E (SPEI-I); EDS ELECTRONIC DATA SYSTEMS FERTIGUNGSIN (EDSE-N)

Inventor: NALEPA E; SPEIDEL E; WISCHNIK A

Number of Countries: 023 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
DE 4229647	C1	19931202	DE 4229647	A	19920904	199348	B
WO 9406089	A1	19940317	WO 93EP2301	A	19930826	199412	
AU 9349540	A	19940329	AU 9349540	A	19930826	199430	
EP 658261	A1	19950621	EP 93919183	A	19930826	199529	
			WO 93EP2301	A	19930826		
EP 658261	B1	19960529	EP 93919183	A	19930826	199626	
			WO 93EP2301	A	19930826		
DE 59302763	G	19960704	DE 502763	A	19930826	199632	
			EP 93919183	A	19930826		
			WO 93EP2301	A	19930826		
JP 8500923	W	19960130	WO 93EP2301	A	19930826	199642	
			JP 94506837	A	19930826		
US 5805729	A	19980908	WO 93EP2301	A	19930826	199843	
			US 95393008	A	19950303		

Priority Applications (No Type Date): DE 4229647 A 19920904

Cited Patents: 3.Jnl.Ref; EP 334230

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

DE 4229647 C1 8 G06F-015/66

WO 9406089 A1 G 25 G06F-015/62

Designated States (National): AU BR CA JP KR US

Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE

AU 9349540 A G06F-015/62 Based on patent WO 9406089

EP 658261 A1 G 1 G06F-015/62 Based on patent WO 9406089

Designated States (Regional): BE CH DE FR GB IT LI LU NL

EP 658261 B1 G 13 G06T-009/20 Based on patent WO 9406089

Designated States (Regional): BE CH DE FR GB IT LI LU NL

DE 59302763 G G06T-009/20 Based on patent EP 800261
Based on patent WO 9406089
JP 8500923 W 23 G06T-007/00 Based on patent WO 9406089
US 5805729 A G06K-009/46 Based on patent WO 9406089

Abstract (Basic): DE 4229647 C

The method involves obtaining line data from the measured values of specific parameters of a measured object at defined measuring points via coding of the measured values which are then stored in a matrix in dependence on the geometric point coordinates of the corresponding measuring points. Coding differences between the coding of a start point element of the matrix and the coding of surrounding elements are identified and represented by a difference signal (GD) and analysed to detect a defined correlation, to provide an index signal (IS).

The matrix elements provided with an index signal are identified, to determine the geometric point coordinates of the measured object joined together by representation lines.

USE - For computer-assisted design in motor vehicle ind.

Dwg.1/1

Title Terms: LINE; DATA; PROVISION; METHOD; CONVERT; MEASURE; PARAMETER; VALUE; STORAGE; CODE; MEASURE; VALUE; MATRIX; EVALUATE; MATRIX; POINT; EXHIBIT; DEFINE; CORRELATE; DETERMINE; OBJECT; POINT; COORDINATE

Index Terms/Additional Words: MOTOR; VEHICLE; CAD

Derwent Class: T01

International Patent Class (Main): G06F-015/62 ; G06F-015/66 ; G06K-009/46; G06T-007/00; G06T-009/20

International Patent Class (Additional): G06F-015/68 ; G06T-005/00

File Segment: EPI

6/5/35 (Item 19 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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009594452

WPI Acc No: 1993-287998/199336

XRPX Acc No: N93-221561

Controlling process priority in semaphore operation for multi-tasking process in data processing system - involves using P operation for occupying resource during execution of first process and V operation for releasing occupied resource during execution of first operation

Patent Assignee: TOSHIBA KK (TOKE)

Inventor: KUBO H

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5241676	A	19930831	US 90523816	A	19900515	199336 B

Priority Applications (No Type Date): JP 89122314 A 19890516

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5241676	A		8	G06F-009/46	

Abstract (Basic): US 5241676 A

The controlling process involves comparing, in the first operation, the semaphore priority with a first process priority of a process which is in the running state. The first process priority is changed into a first changed priority corresponding to the semaphore priority when the semaphore priority is higher than the first process priority, to execute the first process. The first changed priority is returned, in the second operation to the **first process** priority. The semaphore priority is **compared** with a **second process** priority of a **second process** which is in the wait state.

The second process priority is changed into a second changed priority corresponding to the semaphore priority when the semaphore priority is higher than the second process priority. The second process having the second changed priority is set in the ready state. The returning first process priority is changed with the second changed

priority. The first process is set in the ready state when the second changed priority is higher than the returned first process priority. A process having a highest process priority is selected from among the process priorities of the process which are in the ready state to execute the selected process.

ADVANTAGE - Instrumentalities and combinations used to improve system performance.

Dwg.3,4/4

Title Terms: CONTROL; PROCESS; PRIORITY; SEMAPHORE; OPERATE; MULTI; PROCESS
; DATA; PROCESS; SYSTEM; P; OPERATE; OCCUPY; RESOURCE; EXECUTE; FIRST;
PROCESS; OPERATE; RELEASE; OCCUPY; RESOURCE; EXECUTE; FIRST; OPERATE

Derwent Class: T01

International Patent Class (Main): G06F-009/46

File Segment: EPI

00791660

Method and system for providing interoperability among processes written to
execute on different operating systems

Verfahren und System zum Ermöglichen der Zusammenarbeit von zur Ausführung
auf unterschiedlichen Betriebssystemen geschriebenen Prozessen

Methode et système pour fournir l'interopérabilité entre des processus
écrits pour s'exécuter sur des systèmes d'exploitation différents

PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392731), 2550 Garcia Avenue, Mountain View, CA
94303, (US), (Proprietor designated states: all)

INVENTOR:

Kougiouris, Panagiotis, 1200 Dale Avenue No. 132, Mountain View,
California 94040, (US)

Madany, Peter W., 5474 Dekker Terrace, Fremont, California 94555, (US)

Radia, Sanjay R., 883 Boar Circle, Fremont, California 94539, (US)

Shivanlingiah, Anil S., 535 Fallen Leaf Circle, San Ramon, California
94583, (US)

LEGAL REPRESENTATIVE:

W.P. Thompson & Co. (101051), Coopers Building, Church Street, Liverpool
L1 3AB, (GB)

PATENT (CC, No, Kind, Date): EP 737919 A2 961016 (Basic)
EP 737919 A3 970115
EP 737919 B1 020626

APPLICATION (CC, No, Date): EP 96302575 960412;

PRIORITY (CC, No, Date): US 422737 950414

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/455

ABSTRACT WORD COUNT: 303

NOTE:

Figure number on first page: 3

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1891
CLAIMS B	(English)	200226	1271
CLAIMS B	(German)	200226	1248
CLAIMS B	(French)	200226	1392
SPEC A	(English)	EPAB96	5206
SPEC B	(English)	200226	5792
Total word count - document A			7098
Total word count - document B			9703
Total word count - documents A + B			16801

...SPECIFICATION copy of the parent's context object has been instantiated,
processing in the Generate Context Object procedure takes one of two
paths, depending on a **comparison** of a "process type" of the **child**
process with a "process type" of the **parent process** (step 1003). A
"process type" indicates which operating system the process was
initially written to run on top of. For example, if the parent process
321 was initially written to **execute** on top of the Solaris 1.x
operating system then the "process type" of the parent process is
"Solaris 1.x.". The "process type" of...

...SPECIFICATION the context object for the parent process binding at least
one context object associated with the second operating system to a
selected object name;

during **execution** of the parent process, launching a child process
which was designed to **execute** on a third operating system;

obtaining a context object for the **child process** ;

comparing a process type of the **child process** with a process type
of the **parent process** , the **process** type indicating which operating

...the process was initially written to run on top of,

performing a context merge operation on the context object of...

...associated with the second operating system to a selected object name;

a third program mechanism configured to start a child process which was designed to **execute** on a third operating system;

a fourth program mechanism configured to obtain a context object for the child process; a comparator **comparing** a process type of the **child process** with a process type of the **parent process**, the **process** type indicating which operating system the process was initially written to run on top of;

a fifth program mechanism configured to perform a context merge...copy of the parent's context object has been instantiated, processing in the Generate Context Object procedure takes one of two paths, depending on a **comparison** of a "process type" of the **child process** with a "process type" of the **parent process** (step 1003). A "process type" indicates which operating system the process was initially written to run on top of. For example, if the parent process 321 was initially written to **execute** on top of the Solaris 1.x operating system then the "process type" of the parent process is "Solaris 1.x.". The "process type" of...

...CLAIMS the context object for the parent process binding at least one context object associated with the second operating system to a selected object name;

during **execution** of the parent process (321), launching a child process (323) which was designed to **execute** on a third operating system;

obtaining a context object (325) for the child process (323);

comparing (1003) a process type of the **child process** with a process type of the **parent process**, the **process** type indicating which operating system the process was initially written to run on top of;

performing a context merge operation (715) on the context object...

...with the second operating system to a selected object name;

a third program mechanism (323) configured to start a child process which was designed to **execute** on a third operating system;

a fourth program mechanism (329) configured to obtain a context object for the child process; a comparator (1003) **comparing** a process type of the **child process** with a process type of the **parent process**, the **process** type indicating which operating system the process was initially written to run on top of;

a fifth program mechanism (1005) configured to perform a context...

8/3,K/22 (Item 22 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01789101

Method and apparatus for managing a database in a distributed object operating environment

Verfahren und Gerät zum Verwalten einer Datenbank in einer verteilten Objektbetriebsumgebung

Methode et appareil pour gerer une base de donnees dans un environnement d'exploitation d'objets distribue

PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392732), 2550 Garcia Avenue, Mountain View, California 94043-1100, (US), (Proprietor designated states: all)

INVENTOR:

Hapner, Mark W., 595 Brooks Avenue, San Jose, CA 95125, (US)

Cattell, Roderic G., 737 Edge Lane, Los Altos 94024, (US)

LEGAL REPRESENTATIVE:

Browne, Robin Forsythe, Dr. (55142), Urquhart-Dykes & Leach Tower House
Merrion Way, Leeds LS2 8PA, (GB)

PATENT (CC, No, Kind, Date): EP 735473 A2 961002 (Basic)
EP 735473 A3 980204
EP 735473 B1 030903

APPLICATION (CC, No, Date): EP 96301249 960223;

PRIORITY (CC, No, Date): US 414119 950331

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/46; G06F-012/08

ABSTRACT WORD COUNT: 220

NOTE:

Figure number on first page: NONE

LANGUAGE (Publication, Procedural, Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200336	1499
CLAIMS B	(German)	200336	1232
CLAIMS B	(French)	200336	1895
SPEC B	(English)	200336	10961
Total word count - document A			0
Total word count - document B			15587
Total word count - documents A + B			15587

...SPECIFICATION thread may lock a mutex corresponding to a certain condition variable having a data structure which includes a true or false flag, thereby preventing all **other threads** from operating on this condition variable. The **first thread** may then **evaluate** the true or false flag, performing the desired action if the flag value is true. If the flag is false, the thread blocks on the condition flag and simultaneously releases the mutex. When the first thread blocks on the condition flag, it temporarily stops **executing** and waits for the condition flag to change. Then, when a second thread changes the condition flag, it may also broadcast a wakeup call to...

8/3,K/23 (Item 23 from file: 348)

INATALOG(R) File 348:EUROPEAN PATENTS

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00747354

Capability engine method and apparatus for a microkernel data processing system

Verfahren und Gerat mit Fahigkeitsvorrichtung fur ein Mikrokern-Datenverarbeitungssystem

Methode et appareil a dispositif de capacite pour un systeme de traitement de donnees a micro-noyaux

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road, Armonk, N.Y. 10504, (US), (Proprietor designated states: all)

INVENTOR:

Magee, James Michael, 5310 Canal Drive, Lake Worth, Florida 33463, (US)
Sotomayor, Guy Gil, Jr., 6042 Sherwood Glen Way, Apt. 3, West Palm Beach, Florida 33415, (US)

Youngworth, Christopher Dean, 3 Gulfview Court, Savoy, Illinois 61874, (US)

LEGAL REPRESENTATIVE:

Williams, Julian David (75461), IBM United Kingdom Limited, Intellectual Property Department, Hursley Park, Winchester, Hampshire SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 704796 A2 960403 (Basic)
EP 704796 A3 980701
EP 704796 B1 000419

APPLICATION (CC, No, Date): EP 95304188 950616;

PRIORITY (CC, No, Date): US 263313 940928

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT WORD COUNT: 141

NOTE:

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200016	1721
CLAIMS B	(German)	200016	1710
CLAIMS B	(French)	200016	1919
SPEC B	(English)	200016	37863
Total word count - document A			0
Total word count - document B			43213
Total word count - documents A + B			43213

...CLAIMS runs on said first processor;
an I/O processor sharing said memory with said first processor, and
coupled by said communications link with said second processor ;
a second task container in said memory having a set of attributes
defining a second communication port and a second set of port rights,
said second task container having a thread that runs on said I/O
communications processor ; capability engine registering said first
set of port rights for said first task container and said second
set of port rights for said second task container; said
capability engine comparing said first set of port rights and said
second set of port rights to determine if a capability to access said
memory object can be...

8/3,K/24 (Item 24 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00663510

Logic circuit having error detection function, redundant resource
management method, and fault tolerant system using it
Logischer Schaltkreis mit Fehlernachweisfunktion, Verfahren zum Verwalten
von Betriebsmitteln und fehlertolerantes System zu seiner Anwendung
Circuit logique avec fonction de detection d'erreurs, procede de gestion
des ressources redoudantes et systeme tolerant des fautes pour sa mise
en oeuvre

PATENT ASSIGNEE:

Hitachi, Ltd., (204141), 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo
101, (JP), (Proprietor designated states: all)

INVENTOR:

Suzuki, Shoji, 17-1-202, Moriyama-cho 3-chome, Hitachi-shi, Ibaraki 316,
(JP)
Sato, Yoshimichi, Yuuhou-ryo 34, 20-3, Ayukawa-cho 6-chome, Hitachi-shi,
Ibaraki 316, (JP)
Tashiro, Korefumi, 5-1, Oomika-cho 6-chome, Hitachi-shi, Ibaraki 319-12,
(JP)
Bekki, Keisuke, 17-2-503, Moriyama-cho 3-chome, Hitachi-shi, Ibaraki 316,
(JP)
Sato, Hiroshi, 2920-114, Nawatari, Katsuta-shi, Ibaraki 312, (JP)
Nohmi, Makoto, Tsukubadai Terasu 103, 663, Ichige, Katsuta-shi, Ibaraki
312, (JP)

LEGAL REPRESENTATIVE:

Calderbank, Thomas Roger et al (50122), MEWBURN ELLIS York House 23
Kingsway, London WC2B 6HP, (GB)

PATENT (CC, No, Kind, Date): EP 653708 A2 950517 (Basic)
EP 653708 A3 950802
EP 653708 B1 000816

APPLICATION (CC, No, Date): EP 94307483 941012;

PRIORITY (CC, No, Date): JP 93258013 931015; JP 9427664 940225

DESIGNATED STATES: DE; FR; GB

RELATED DIVISIONAL NUMBER(S) - PN (AN):

EP 1016968 (EP 100479)

INTERNATIONAL PATENT CLASS: G06F-011/16; G06F-011/00; G06F-011/20

ABSTRACT WORD COUNT: 170

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200033	751
CLAIMS B	(German)	200033	578
CLAIMS B	(French)	200033	885
SPEC B	(English)	200033	15210
Total word count - document A			0
Total word count - document B			17424
Total word count - documents A + B			17424

8/3,K/25 (Item 25 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00632850

Speech dialogue system

Sprachdialogsystem

Systeme de dialogue vocale

PATENT ASSIGNEE:

NEC CORPORATION, (236690), 7-1, Shiba 5-chome, Minato-ku, Tokyo, (JP),
(Proprietor designated states: all)

INVENTOR:

Hatazaki, Kaichiro, c/o NEC CORPORATION, 7-1, Shiba 5-chome, Minato-ku,
Tokyo, (JP)

LEGAL REPRESENTATIVE:

Betten & Resch (101031), Postfach 10 02 51, 80076 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 615228 A1 940914 (Basic)

EP 615228 B1 010718

APPLICATION (CC, No, Date): EP 94103604 940309;

PRIORITY (CC, No, Date): JP 9348085 930309

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G10L-015/22; G10L-015/28

ABSTRACT WORD COUNT: 120

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	1489
CLAIMS B	(English)	200129	1195
CLAIMS B	(German)	200129	966
CLAIMS B	(French)	200129	1293
SPEC A	(English)	EPABF2	6440
SPEC B	(English)	200129	5520
Total word count - document A			7930
Total word count - document B			8974
Total word count - documents A + B			16904

...SPECIFICATION dividing portion 40 as checked at the step 402 before completion of the current execution of the first application process, the second application process is **executed** for the currently processed partial string (step 403). Before completion of the second application process for the current partial string, check is performed whether next partial string is received from the input dividing portion 40 (step 404). If input of the next partial string is **judged** at the step 404, the **execution** of the **second** application **process** is interrupted (step 405), and the **first** application **process** is instantly **executed**. When no next partial string is input from the input dividing portion 40 as checked at the step 404 before completion of the second application process, the second application process for the current partial string is continued. On the other hand, when the **execution** of the second application process is interrupted at the step 405, the execution is resumed in advance of initiation of execution of the second application

... SPECIFICATION dividing portion 40 as checked at the step 402 before completion of the current execution of the first application process, the second application process is **executed** for the currently processed partial string (step 403). Before completion of the second application process for the current partial string, check is performed whether next partial string is received from the input dividing portion 40 (step 404). If input of the next partial string is **judged** at the step 404, the **execution** of the **second application process** is interrupted (step 405), and the **first application process** is instantly **executed**. When no next partial string is input from the input dividing portion 40 as checked at the step 404 before completion of the second application process, the second application process for the current partial string is continued. On the other hand, when the **execution** of the second application process is interrupted at the step 405, the execution is resumed in advance of initiation of execution of the second application

8/3,K/26 (Item 26 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
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00629642

Multitask processing unit

Multitaskingverarbeitungseinheit

Unite de traitement multi-tache

PATENT ASSIGNEE:

DENSO CORPORATION, (211491), 1-1, Showa-cho,, Kariya-City, Aichi-Pref., (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 613085 A2 940831 (Basic)

EP 613085 A3 950719

EP 613085 B1 990609

APPLICATION (CC, No, Date): EP 94102917 940225;

PRIORITY (CC, No, Date): JP 3826993 930226; JP 3827093 930226; JP 3826793 930226; JP 3826893 930226

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-009/46;

ABSTRACT WORD COUNT: 166

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9923	888
CLAIMS B	(German)	9923	807
CLAIMS B	(French)	9923	1025
SPEC B	(English)	9923	4744
Total word count - document A			0
Total word count - document B			7464
Total word count - documents A + B			7464

... CLAIMS switched by said generating means (39), wherein said program of said first task (L-task) is prohibited from branch instructions, and is composed to be **executed** fix-looped from its start address to a specific address, with at least one routine of runaway monitor and timer for said second task (A-task) incorporated.

- The multitask processing unit according to claim 1, characterized in that each instruction of the programs of said **first** and **second tasks** includes a task identification bit (30) to indicate the kind of task, and further comprising a task discrimination means (31) for

checking a content....

- ...which are sending a no-operation instruction in place of each instruction of said programs to the executing means, when said task discrimination means (31) judges that the content of the task identification bit and the **execution** task does not conform.
5. The multitask processing unit according to claim 4, characterized in that a parity bit is provided for said task identification bit.
 6. The multitask processing unit according to claim 1, characterized in that said **executing** means (CPU0, CPU1) has first and second registers (21, 22) installed in correspondence with each of the programs of the said **first** and **second tasks**.
 7. The multitask processing unit according to claim 6, characterized in that said first and second registers (21, 22) are alternately switched based on the switching signal from the generating means (39).
 8. The multitask processing unit according to claim 7, characterized in that said **executing** means (CPU0, CPU1), when fetching a branch instruction, finishes setting the branch address one cycle before the next instruction fetch stage of the program in...
- ...1, characterized in that said program memory (12) stores a data area (29) to be used for operation, each instruction of the programs of the **first** and **second tasks** indicates whether it is a caution-requiring instruction or not by a small number bit (36, 37) in the instruction, and the data area is...

8/3,K/27 (Item 27 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00569038

ATM policing function with autonomous reference time instants

ATM-Überwachungsfunktion mit autonomen Referenzzeitpunkten

Function de surveillance ATM avec des instants de temps de reference autonomes

PATENT ASSIGNEE:

Koninklijke KPN N.V., (1066890), Stationsplein 7, 9726 AE Groningen, (NL), (Proprietor designated states: all)

INVENTOR:

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Feijen, Maurice Mathias, Prinses Beatrixlaan 100, NL-2286 LC Rijswijk, (NL)

PATENT (CC, No, Kind, Date): EP 555925 A1 930818 (Basic)
EP 555925 B1 990915

APPLICATION (CC, No, Date): EP 93200347 930210;

PRIORITY (CC, No, Date): NL 92253 920212

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IE; IT; LI; LU; NL; PT; SE

INTERNATIONAL PATENT CLASS: H04L-012/56

ABSTRACT WORD COUNT: 178

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; Dutch

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9937	243
CLAIMS B	(German)	9937	225
CLAIMS B	(French)	9937	272
SPEC B	(English)	9937	3749
Total word count - document A			0
Total word count - document B			4489
Total word count - documents A + B			4489

...SPECIFICATION data cell) and for the second process (the autonomous updating of the counter position of one or more (other) channels. An exact timing between the **first** (main) **process** and the **second**

process will, however, require additional measures because of the so-called contention problem, which may occur if both the (data-cell-controlled) first process and the (autonomous, processor-controlled) second process have to

8/3,K/28 (Item 28 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00435180

Parallel processing trace data manipulation
Bearbeitung von Ablaufdaten paralleler Verarbeitung
Manipulation de donnees de trace de traitement en parallele
PATENT ASSIGNEE:

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Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

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PATENT (CC, No, Kind, Date): EP 422945 A2 910417 (Basic)
EP 422945 A3 920304
EP 422945 B1 960814

APPLICATION (CC, No, Date): EP 90311178 901011;

PRIORITY (CC, No, Date): US 420845 891013

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/32; G06F-011/34;

ABSTRACT WORD COUNT: 63

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB96	525
CLAIMS B	(German)	EPAB96	579
CLAIMS B	(French)	EPAB96	611
SPEC B	(English)	EPAB96	5484
Total word count - document A			0
Total word count - document B			7199
Total word count - documents A + B			7199

...CLAIMS indexed by selected processor, task identification, and parallel activity.

- The method of any of claims 2, 3, or 4, wherein said graphical mapping of **processor** utilization includes a time process diagram, each time process diagram being susceptible to a zoom, forward, and backward view expressed as a time range or interval, and said mapping step further includes the steps of expressing parallel activity in the form of **primary** and **secondary threads**, a parallel thread being a unit of work eligible for concurrent **execution**, **comparing** the beginning and end times for each primary thread to the time range of the selected view, and selectively adjusting the view to conform the...

8/3,K/29 (Item 29 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00435181

Image processing apparatus
Bildverarbeitungsgerat
Dispositif pour le traitement d'image
PATENT ASSIGNEE:

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Tokyo, (JP), (applicant designated states: DE;FR;GB;IT)

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Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick
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PATENT (CC, No, Kind, Date): EP 395452 A2 901031 (Basic)
EP 395452 A3 920122
EP 395452 B1 970702

APPLICATION (CC, No, Date): EP 90304683 900430;

PRIORITY (CC, No, Date): JP 89109511 890428; JP 89117020 890510

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: H04N-001/46;

ABSTRACT WORD COUNT: 176

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1182
CLAIMS B	(English)	EPAB97	683
CLAIMS B	(German)	EPAB97	566
CLAIMS B	(French)	EPAB97	802
SPEC A	(English)	EPABF1	6653
SPEC B	(English)	EPAB97	6395
Total word count - document A			7835
Total word count - document B			8446
Total word count - documents A + B			16281

...ABSTRACT A2

There is provided an image processing apparatus for **executing** a process to an input image in accordance with the characteristics thereof, comprising: an input device to input image data; a detector to detect the characteristics of the input image; and a processor to execute first and **second processes** such as brightness correcting processes or color balance correcting processes for the input image data on the basis of **weights** corresponding to the characteristics of the input image. When the **weight** of the **first process** is large, the **weight** of the **second process** is set to a small value. As the characteristics of the input image are deviated from a predetermined condition, the processor sets the **weight** of the **first process** to a small value and sets the **weight** of the **second process** to a large value. With the apparatus, the brightness and the color balance of the input image are preferably corrected and a natural image can...

...CLAIMS 1. An image processing apparatus comprising:

- inputting means for inputting image data;
 - detecting means for detecting characteristics of an input image;
 - and
 - processing means for **executing first and second processes** to the input image data on the basis of **weights** corresponding to the characteristics of the input image.
2. An image processing apparatus comprising:
- inputting means for inputting image data;
 - detecting means for detecting characteristics of an input image;
 - and
 - processing means for **executing first and second processes** to the input image data on the basis of **weights** corresponding to a deviation of the characteristics of the input image from a predetermined condition.
3. An image processing apparatus comprising:
- inputting means for inputting image data;
 - detecting means for detecting characteristics of an input image;
 - and
 - processing means for **executing** a first process to be executed when the input image is an image which is close to a predetermined condition and a **second process** which is different from the **first process** for the input image data,
- wherein said processing means executes the **first** and **second**

processes on the basis of weights corresponding to the characteristics of the input image, respectively.

4. An apparatus according to claim 3, wherein as the characteristics of the input image are deviated from the predetermined condition, said processing means sets the weight of the first process to a small value and sets the weight of the second process to a large value.
5. An apparatus according to claim 3, wherein the second process is executed when the characteristics of the input image are far from the predetermined condition.
6. An image processing apparatus comprising:
inputting means for inputting image data...

...for extracting first and second image data from the input image data;
detecting means for detecting characteristics of an input image;
and
processing means for executing a first process based on the first image data and a second process based on the second image data for the input image data,
wherein said processing means executes the first and second processes on the basis of weights corresponding to the characteristics of the input image.

7. An image processing apparatus comprising:
inputting means for inputting image data;
extracting means for extracting first and second image data from the input image data;
detecting means for detecting characteristics of an input image;
and
processing means for executing a first brightness correcting process based on the first image data and a second brightness correcting process based on the second image data for the input image data on the basis of weights corresponding to the characteristics of the input image.

8...

...claims 1, 6, 7 and 8, wherein as the characteristics of the input image are deviated from a predetermined condition, said processing means sets the weight of the first process to a small value and sets the weight of the second process to a large value.

11. An apparatus according to any one of claims 1, 2, 6, 7 and 8 wherein the first process is executed when the characteristics of the input image are close to a predetermined condition, and said second process is executed when the characteristics of the input image are far from the predetermined condition.
12. An apparatus according to any one of claims 1, 2, 3...

8/3,K/30 (Item 30 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00393374

Adaptive analysis apparatus.

Adaptives Analysegerat.

Appareil d'analyse adaptatif.

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 399386 A2 901128 (Basic)
EP 399386 A3 911218

APPLICATION (CC, No, Date): EP 90109406 900518;
PRIORITY (CC, No, Date): US 356738 890525
DESIGNATED STATES: DE; FR; GB; IT; SE
INTERNATIONAL PATENT CLASS: H03H-021/00;
ABSTRACT WORD COUNT: 230

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	756
SPEC A	(English)	EPABF1	6326
Total word count - document A			7082
Total word count - document B			0
Total word count - documents A + B			7082

...SPECIFICATION and third reference sensors are coupled to the first, third and fifth processors 56a, 56c, 56e, respectively. Data processed by the first, third and fifth **processors** 56a, 56c, 56e are coupled by way of latches 54b, 54d, 54f to the second, fourth and sixth **processors** 56b, 56d, 56f. The first and second **processors** 56a, 56b implement the Least Means Square adaptive filter having distributed **weights**. In particular, the **first processor** 56a **processes** the first 200 **weights**, while the **second processor** 56b **processes** the second 200 **weights**. Similarly, the third and fourth **processors** 56c, 56d, and the fifth and sixth **processors** 56e, 56f implement least means square adaptive filters having distributed weights, respectively. Additionally, the second **processor** 56b combines the primary signal with the adaptively-filtered noise signals processed by the adaptive filters and generates coefficient update terms for each of the...

8/3,K/31 (Item 31 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00393363

Multi-channel adaptive canceler

Adaptiver Kompensator mit mehreren Kanalen

Suppresseur adaptatif a multiples canaux

PATENT ASSIGNEE:

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Los Angeles, California 90045-0066, (US), (applicant designated states:
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INVENTOR:

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Witte, Alexander, Dr.-Ing. (46523), Witte, Weller, Gahlert, Otten &
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PATENT (CC, No, Kind, Date): EP 399382 A2 901128 (Basic)
EP 399382 A3 911227
EP 399382 B1 960911

APPLICATION (CC, No, Date): EP 90109393 900518;
PRIORITY (CC, No, Date): US 356736 890525
DESIGNATED STATES: DE; FR; GB; IT; SE
INTERNATIONAL PATENT CLASS: H03H-021/00;
ABSTRACT WORD COUNT: 106

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	681
CLAIMS B	(English)	EPAB96	651
CLAIMS B	(German)	EPAB96	582
CLAIMS B	(French)	EPAB96	736
SPEC A	(English)	EPABF1	5212
SPEC B	(English)	EPAB96	5473
Total word count - document A			5893

Total word count - document B 7442
Total word count - documents A + B 13335

...SPECIFICATION and third reference sensors are coupled to the first, third and fifth processors 56a, 56c, 56e, respectively. Data processed by the first, third and fifth processors 56a, 56c, 56e are coupled by way of latches 54b, 54d, 54f to the second, fourth and sixth processors 56b, 56d, 56f. The first and second processors 56a, 56b implement the Least Means Square adaptive filter having distributed weights. In particular, the first processor 56a processes the first 200 weights, while the second processor 56b processes the second 200 weights. Similarly, the third and fourth processors 56c, 56d, and the fifth and sixth processors 56e, 56f implement least means square adaptive filters having distributed weights, respectively. Additionally, the second processor 56b combines the primary signal with the adaptively-filtered noise signals processed by the adaptive filters and generates coefficient update terms for each of the...

...SPECIFICATION and third reference sensors are coupled to the first, third and fifth processors 56a, 56c, 56e, respectively. Data processed by the first, third and fifth processors 56a, 56c, 56e are coupled by way of latches 54b, 54d, 54f to the second, fourth and sixth processors 56b, 56d, 56f. The first and second processors 56a, 56b implement the Least Means Square adaptive filter having distributed weights. In particular, the first processor 56a processes the first 200 weights, while the second processor 56b processes the second 200 weights. Similarly, the third and fourth processors 56c, 56d, and the fifth and sixth processors 56e, 56f implement least means square adaptive filters having distributed weights, respectively. Additionally, the second processor 56b combines the primary signal with the adaptively-filtered noise signals processed by the adaptive filters and generates coefficient update terms for each of the...

8/3,K/32 (Item 32 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00388698

An error absorbing system in a neuron computer

Fehler absorbierendes System in einem neuronalen Rechner

Systeme de consommation d'erreur dans un ordinateur neuronal

PATENT ASSIGNEE:

FUJITSU LIMITED, (211460), 1015, Kamikodanaka, Nakahara-ku, Kawasaki-shi, Kanagawa 211, (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

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Tsuzuki, Hiroyuki, 102-108, 3-22, Yurigaoka, Asao-ku, Kawasaki-shi, Kanagawa, 215, (JP)

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Kawasaki, Takashi, 409-1, Miyauchi, Nakahara-ku, Kawasaki-shi, Kanagawa, 211, (JP)

Matsuda, Toshiharu, 3-5-9, Sugeshengoku, Tama-ku, Kawasaki-shi, Kanagawa, 214, (JP)

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Tsuchiya, Chikara, 6-3-35, Tamagawakuen, Machida-shi, Tokyo, 194, (JP)

Ishikawa, Katsuya, 123-4, Miyazaki, Miyamae-ku, Kawasaki-shi, Kanagawa, 213, (JP)

LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 385436 A2 900905 (Basic)
EP 385436 A3 930127
EP 385436 B1 960501
APPLICATION (CC, No, Date): EP 90103900 900228;
PRIORITY (CC, No, Date): JP 8945208 890228; JP 8952974 890307; JP 8954671 890307
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS: G06F-015/80;
ABSTRACT WORD COUNT: 260

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1529
CLAIMS B	(English)	EPAB96	1403
CLAIMS B	(German)	EPAB96	1190
CLAIMS B	(French)	EPAB96	1638
SPEC A	(English)	EPABF1	26157
SPEC B	(English)	EPAB96	25962
Total word count - document A			27689
Total word count - document B			30193
Total word count - documents A + B			57882

...SPECIFICATION amplifier forming ANPs 1 to 5 is output from A/D converter 707.

Next, a model for producing an operational error in an analog neuron **processor** and a method of correcting the weight utilizing a dummy node is explained. Figure 31 shows a conceptual view of the **algorithm** of primary correction and **secondary** correction **processes**. The **primary** correction **process** is to set a **measurement** condition for presuming an amplifier gain and for processing a measurement of offset voltage. When the flowchart starts, the fixed voltage of the dummy node...

...SPECIFICATION amplifier forming ANPs 1 to 5 is output from A/D converter 707.

Next, a model for producing an operational error in an analog neuron **processor** and a method of correcting the weight utilizing a dummy node is explained. Figure 31 shows a conceptual view of the **algorithm** of primary correction and **secondary** correction **processes**. The **primary** correction **process** is to set a **measurement** condition for presuming an amplifier gain and for processing a measurement of offset voltage. When the flowchart starts, the fixed voltage of the dummy node...

8/3,K/33 (Item 33 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00350291

Method and apparatus for communicating data between multiple tasks in data processing systems.

Verfahren und Vorrichtung für die Datenübertragung zwischen mehreren Aufgaben in Datenverarbeitungssystemen.

Methode et dispositif pour la communication des données entre multiples tâches dans des systèmes de traitement des données.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road, Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

George, David Alson, Arthur Court, Somers New York 10589, (US)

Kathi, Bharat Deep, 167-C, Rt. 8, Lakeview Drive, Mahopac New York 10541, (US)

LEGAL REPRESENTATIVE:

Jost, Ottokarl, Dipl.-Ing. (6092), IBM Deutschland Informationssysteme GmbH, Patentwesen und Urheberrecht, D-70548 Stuttgart, (DE)

PATENT (CC, No, Kind, Date): EP 361176 A2 900404 (Basic)
EP 361176 A3 920826
EP 361176 B1 950419

APPLICATION (CC, No, Date): EP 89116644 890908;
PRIORITY (CC, No, Date): US 250673 880929
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS: G06F-009/46;
ABSTRACT WORD COUNT: 213

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	818
CLAIMS B	(English)	EPAB95	806
CLAIMS B	(German)	EPAB95	734
CLAIMS B	(French)	EPAB95	931
SPEC A	(English)	EPABF1	8737
SPEC B	(English)	EPAB95	8782
Total word count - document A			9556
Total word count - document B			11253
Total word count - documents A + B			20809

...SPECIFICATION completion of its program, the processing element 12 running each child task generates a second CAN directive to determine if the data generated by that **child task** has been collected by the **parent task**. This CAN directive includes a **comparison** value of "-1", a requesting processing element identification (and a task identification where appropriate), the address of FLAG2, and a TIMEOUT value determined by the process. If data returned to the requesting processing element 12 as a function of the first **execution** of the CAN directive indicates that there is no match between the comparison value and the value stored at FLAG1, the requesting processing element 12...

...SPECIFICATION completion of its program, the processing element 12 running each child task generates a second CAN directive to determine if the data generated by that **child task** has been collected by the **parent task**. This CAN directive includes a **comparison** value of "-1", a requesting processing element identification (and a task identification where appropriate), the address of FLAG2, and a TIMEOUT value determined by the process. If data returned to the requesting processing element 12 as a function of the first **execution** of the CAN directive indicates that there is no match between the comparison value and the value stored at FLAG1, the requesting processing element 12...

8/3,K/34 (Item 34 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00319190

Image forming apparatus.

Bilderzeugungsgerat.

Appareil de formation d'images.

PATENT ASSIGNEE:

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Aoki, Tomohiro, 2-45-39, Mutsukawa Minami-ku, Yokohama-shi Kanagawa-ken, (JP)

Murayama, Yasushi, 4-1-31-304, Kamata Setagaya-ku, Tokyo, (JP)

Hirose, Yoshihiko, 4-20-15-202, Azamino Midori-ku, Yokohama-shi Kanagawa-ken, (JP)

Uchida, Takashi, 6-4-25, Tsunashimanishi Kohoku-ku, Yokohama-shi Kanagawa-ken, (JP)

Matsuzawa, Kunihiro, 818, Kamimarukohachimancho Nakahara-ku, Kawasaki-shi Kanagawa-ken, (JP)

Kanekura, Kazunori, 1171-9, Ichigaocho Midori-ku, Yokohama-shi Kanagawa-ken, (JP)

LEGAL REPRESENTATIVE:

Beresford, Keith Denis Lewis et al (28273), BERESFORD & Co. 2-5 Warwick

Court High Holborn, London WC1R 5DJ, (GB)
PATENT (CC, No, Kind, Date): EP 319241 A2 890607 (Basic)
EP 319241 A3 910109
EP 319241 B1 940608
APPLICATION (CC, No, Date): EP 88311288 881129;
PRIORITY (CC, No, Date): JP 87300007 871130
DESIGNATED STATES: DE; GB; IT; NL
INTERNATIONAL PATENT CLASS: G03G-015/01; H04N-001/46;
ABSTRACT WORD COUNT: 139

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPBBF1	1686
CLAIMS B	(English)	EPBBF1	442
CLAIMS B	(German)	EPBBF1	405
CLAIMS B	(French)	EPBBF1	507
SPEC A	(English)	EPBBF1	9694
SPEC B	(English)	EPBBF1	8056
Total word count - document A			11380
Total word count - document B			9410
Total word count - documents A + B			20790

...SPECIFICATION things as those in Fig.6.

As will be seen from this Figure, in the second correction control process, image misregistration correcting sequence is forcibly **executed** after the print start signal STR is set high as a result of actuating start key 34. The correcting operation itself is the same as that in the **first** correction control **process**. The **second** correction control **process** is illustrated in Fig. 9.

Step S 21 **judges** whether the print start signal STR has been set high (H). The process proceeds to Step S 22 if the start key 34 has been...

8/3,K/35 (Item 35 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00306062

Digital data processing system.

Digitales Datenverarbeitungssystem.

Systeme du traitement de donnees numeriques.

PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581
, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

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Gavrin, Edward S., Beaver Pond Road RFD 4, Lincoln Massachusetts 01773,
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PATENT (CC, No, Kind, Date): EP 300516 A2 890125 (Basic)
EP 300516 A3 890426
EP 300516 B1 931124

APPLICATION (CC, No, Date): EP 88200921 820521;

PRIORITY (CC, No, Date): US 266413 810522; US 266539 810522; US 266521
810522; US 266415 810522; US 266409 810522; US 266424 810522; US 266421

810522; US 266404 810522; US 266414 810522; US 266532 810522; US 266403
810522; US 266408 810522; US 266401 810522; US 266524 810522
DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE
RELATED PARENT NUMBER(S) - PN (AN):
EP 67556 (EP 823025960)
INTERNATIONAL PATENT CLASS: G06F-009/46; G06F-012/14;
ABSTRACT WORD COUNT: 122

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1018
CLAIMS B	(German)	EPBBF1	868
CLAIMS B	(French)	EPBBF1	1115
SPEC B	(English)	EPBBF1	154256
Total word count - document A			0
Total word count - document B			157257
Total word count - documents A + B			157257

...SPECIFICATION start up, and can be directly read or loaded with test and diagnostic signals for fault monitoring and identification. In addition, as described further below, **microinstructions** may be loaded into JP 10114's microcode **circuitry** at system start up or as required.

Having described the general structure and operation of Computer System 10110, certain features of Computer System 10110 will next be briefly **described** to aid in understanding the following, more detailed descriptions of these and other features of Computer System 10110.

c. Definition of Certain Terms

Certain terms...

8/3,K/36 (Item 36 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00306058

Digital data processing system.

Digitales Datenverarbeitungssystem.

Systeme de traitement de donnees numeriques.

PATENT ASSIGNEE:

DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581
, (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

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Bratt, Richard Glenn, 9 Brook Trail Road, Wayland Massachusetts 01778,
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Clancy, Gerald F., 13069 Jaccaranda Center, Saratoga California 95070,
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27607, (US)

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London WC1X 8PL, (GB)

PATENT (CC, No, Kind, Date): EP 290111 A2 881109 (Basic)
EP 290111 A3 890503
EP 290111 B1 931222

APPLICATION (CC, No, Date): EP 88200917 820521;

PRIORITY (CC, No, Date): US 266404 810522

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 67556 (EP 823025960)

INTERNATIONAL PATENT CLASS: G06F-009/30;

ABSTRACT WORD COUNT: 123

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1044
CLAIMS B	(German)	EPBBF1	890
CLAIMS B	(French)	EPBBF1	1185
SPEC B	(English)	EPBBF1	154314
Total word count - document A			0
Total word count - document B			157433
Total word count - documents A + B			157433

...SPECIFICATION Logical Page Number 1308 into the number of Frame 1306 in
MEM 112 which contains the page.

The virtual memory management system must therefore perform **two** kinds
of translations: (1) AON-offset addresses into AON-page
number-displacement addresses, and (2) AON-page number into a frame
number.

10. Access Control (Fig. 14)

Each time a reference is made to an Object, KOS 706, 710 checks whether
the reference is legal. The **following** discussion will **first** present
the logical structure of access control in CS 101, and then discuss the
microcode and devices which implement it.

CS 101 defines access in...

8/3,K/37 (Item 37 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00226936

System and method for data processing.

Datenverarbeitungssystem und Verfahren.

Systeme et methode de traitement de donnees.

PATENT ASSIGNEE:

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INVENTOR:

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Barstow, David, 64 Heather Lane, Wilton Connecticut 06897, (US)

LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 214037 A2 870311 (Basic)
EP 214037 A3 900516
EP 214037 B1 951004

APPLICATION (CC, No, Date): EP 86401800 860812;

PRIORITY (CC, No, Date): US 767409 850820

DESIGNATED STATES: DE; FR; GB; IT; NL

INTERNATIONAL PATENT CLASSIFICATION: G06F-017/40; G06F-009/46; E21 0047/00;
ABSTRACT WORD COUNT: 216

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	869
CLAIMS B	(English)	EPAB95	1174
CLAIMS B	(German)	EPAB95	1101
CLAIMS B	(French)	EPAB95	1397
SPEC A	(English)	EPABF1	11501
SPEC B	(English)	EPAB95	10934
Total word count - document A			12370
Total word count - document B			14606
Total word count - documents A + B			26976

...CLAIMS sequential data items;

first store means (178) coupled to said first acquisition means
for receiving and storing data items only from the stream of data;

processor means (188, 196, 208) for performing first and
second concurrent **processes**, the **first process** comprising
first type sensor data **evaluation** and time-critical sensor control
according to the result of said evaluation and the second process
(208) for sequential, non time-critical processing of second type
sensor data, the **processor** means being capable of reading data
items from said first store means (178) and incapable of writing new
data items to said stream of data...

File 348:EUROPEAN PATENT 978-2004/Feb W05

(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040304,UT=20040226

(c) 2004 WIPO/Univentio

Set	Items	Description
S1	26370	(1ST OR FIRST OR PRIMARY OR INITIAL OR PARENT OR ROOT) (2W) - (PROCESS OR PROCESSES OR THREAD? ? OR TASK? ? OR JOB? ?)
S2	124348	(2ND OR SECOND? OR CHILD OR SUBSEQUENT OR FOLLOWING OR ENS- UING OR OTHER OR ANOTHER OR DIFFERENT) (2W) (PROCESS OR PROCESS- ES OR THREAD? ? OR TASK? ? OR JOB? ?)
S3	362	(COMPAR? OR CONTRAST? OR WEIGH? OR JUDG? OR MATCH??? OR EV- ALUAT? OR ASSESS? OR MEASUR?) (7N)S1(7N)S2
S4	344	S1(7N)S2(7N) (COMPARE? ? OR COMPARING OR COMPARISON? ? OR C- ONTRAST? OR WEIGH? OR JUDG? OR MATCH??? OR EVALUAT? OR ASSESS? OR MEASUR?)
S5	2347	(1ST OR FIRST OR PRIMARY OR INITIAL OR PARENT OR ROOT) (2W) - THREAD? ?
S6	5040	(2ND OR SECOND? OR CHILD OR SUBSEQUENT OR FOLLOWING OR ENS- UING OR OTHER OR ANOTHER OR DIFFERENT) (2W)THREAD? ?
S7	46	S5(7N)S6(7N) (COMPARE? ? OR COMPARING OR COMPARISON? ? OR C- ONTRAST? OR WEIGH? OR JUDG? OR MATCH??? OR EVALUAT? OR ASSESS? OR MEASUR?)
S8	57	S4(50N) (ALGORITHM? ? OR EXECUT??? OR PROCESSOR? ? OR MICRO- PROCESSOR? ?)
S9	65	S1(5N)S2(5N) (COMPARE? ? OR COMPARING OR COMPARISON? ?)
S10	50	S9 NOT S7:S8

00921498

Method and apparatus for thread synchronization in object-based systems
Verfahren und Gerat zur Fadensynchronisierung in objektbasierten Systemen
Procede et dispositif pour synchronisation de fils dans des systemes bases
sur des objets

PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392737), 901 San Antonio Road, MS PAL1-521,
Palo Alto, California 94043, (US), (Proprietor designated states: all)

INVENTOR:

Bak, Lars, 3782 Corina Way, Palo Alto, California 94303, (US)
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PATENT (CC, No, Kind, Date): EP 840215 A1 980506 (Basic)
EP 840215 B1 020918

APPLICATION (CC, No, Date): EP 97308415 971022;

PRIORITY (CC, No, Date): US 743484 961104; US 57050 P 970827

DESIGNATED STATES: DE; FR; IT; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/46; G06F-009/44

ABSTRACT WORD COUNT: 125

NOTE:

Figure number on first page: 3

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199819	1412
CLAIMS B	(English)	200238	1416
CLAIMS B	(German)	200238	1378
CLAIMS B	(French)	200238	1548
SPEC A	(English)	199819	14597
SPEC B	(English)	200238	14513
Total word count - document A			16012
Total word count - document B			18855
Total word count - documents A + B			34867

...SPECIFICATION 804 to the priority of third thread 806 would permit
second thread 804 to be run substantially as frequently as third thread
806.

In turn, **second** thread 804 may run, and may itself determine that it
has received the sentinel value for first thread 802 when attempting to
study object 808...

...object 808, includes a pointer into the execution stack of first thread
802. Second thread 802 may use this pointer into the execution stack of
first thread 802 to identify **second thread** 802, and may use that
identification to boost the priority of **first thread** 802 to match the
priority of **second thread** 806. Boosting the priority of **first**
thread 802 to the priority of **second thread** 804 would permit **first**
thread 802 to be run as frequently as **second thread** 806. As will be
appreciated by those skilled in the art, the priority of the running
thread, third thread 806, may be propagated to second...

...SPECIFICATION 804 to the priority of third thread 806 would permit
second thread 804 to be run substantially as frequently as third thread
806.

In turn, **second** thread 804 may run, and may itself determine that it
has received the sentinel value for first thread 802 when attempting to
study object 808...

...object 808, includes a pointer into the execution stack of first thread
802. Second thread 802 may use this pointer into the execution stack of

first thread 802 to identify second thread 802, and may use that identification to boost the priority of first thread 802 to match the priority of second thread 806. Boosting the priority of first thread 802 to the priority of second thread 804 would permit first thread 802 to be run as frequently as second thread 806. As will be appreciated by those skilled in the art, the priority of the running thread, third thread 806, may be propagated to second...

7/3,K/6 (Item 6 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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1392730

Method for reserving resources
Methode zum Reservieren von Betriebsmitteln
Methode pour reserver des ressources
PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392730), 2550 Garcia Avenue, Mountain View, CA 94043, (US), (Applicant designated States: all)

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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 817041 A2 980107 (Basic)
EP 817041 A3 991117

APPLICATION (CC, No, Date): EP 97304580 970626;

PRIORITY (CC, No, Date): US 671313 960701

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT WORD COUNT: 118

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9802	2740
SPEC A	(English)	9802	2842
Total word count - document A			5582
Total word count - document B			0
Total word count - documents A + B			5582

...CLAIMS being unavailable or the amount of the first resource available to be reserved being below the predetermined threshold, the resource manager performing a predetermined logical comparison of the first request of the first thread to a second request of a second thread, said resource manager having reserved at least a part of the first resource for the second request in advance of allocating the first resource to the second thread; and
in response to the first request of the first thread exceeding the second request of the second thread in the logical comparison, the resource manager temporarily suspending the second request of the second thread and reserving at least a part of the first resource for the first...

...with the first resource.

13. The computer-implemented method of claim 12, further includes the step of:

in response to the second request of the second thread exceeding the first request of the first thread in the logical comparison, the resource manager rejecting the first request of the first thread.

14. The computer-implemented method of the claim 13, wherein the resource manager is...being unavailable or the amount of the first resource available to be reserved being below the predetermined threshold, the resource manager performing a predetermined logical **comparison** of the first request of the **first thread** to a second request of a **second thread**, said resource manager having reserved at least a part of the first resource for the second request in advance of allocating the first resource to the **second thread**; and in response to the first request of the **first thread** exceeding the second request of the **second thread** in the logical **comparison**, the resource manager temporarily suspending the second request of the second thread and reserving at least a part of the first resource for the first...

...thereon additional instructions which, when executed by a processor, cause said processor to perform the step of:

in response to the second request of the **second thread** exceeding the first request of the **first thread** in the logical **comparison**, the resource manager rejecting the first request of the first thread.

28. The computer-readable medium of the claim 27, wherein the resource manager is...by the first request being unavailable or the amount of the first resource available to be reserved being below the predetermined threshold, a predetermined logical **comparison** of the first request of the **first thread** to a second request of a **second thread**, said resource manager having reserved at least a part of the first resource for the second request in advance of allocating the first resource to the second thread; and

the resource manager further configured to temporarily suspend, in response to the first request of the **first thread** exceeding the second request of the **second thread** in the logical **comparison**, the second request of the second thread and reserve at least a part of the first resource for the first request of the first thread...

...The computer system of claim 40, wherein the resource manager is configured to reject the first request in response to the second request of the **second thread** exceeding the first request of the **first thread** in the logical **comparison**.

42. The computer system of the claim 41, wherein the resource manager is part of an operating system of a limited resource computer system.

7/3,K/7 (Item 7 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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846122

Container closure assembly with profiled screw threads

BEHALTERVERSCHLUSSANORDNUNG MIT PROFILIERTEN SCHRAUBGEWINDEGANGEN

ENSEMBLE FERMETURE POUR RECIPIENT COMPRENANT DES FILETAGES DE VIS PROFILES

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 868358 A2 981007 (Basic)

EP 868358 B1 020731

WO 9721602 970619

APPLICATION (CC, No, Date): EP 96941746 961209; WO 96GB3028 961209

PRIORITY (CC, No, Date): GB 9525146 951208; GB 9525876 951218

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: B65D-041/04

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200231	1088
CLAIMS B	(German)	200231	980
CLAIMS B	(French)	200231	1328
SPEC B	(English)	200231	6937
Total word count - document A			0
Total word count - document B			10333
Total word count - documents A + B			10333

...SPECIFICATION seen that the main advantages of a bayonet-type thread are achieved, whilst retaining all of the advantages of a continuous helix screw threads.

The **first** and **second thread** segments when the closure is in the fully closed and sealing position make a **matching** fit over a major part of the upper surface of the elongate first thread segments. This helps to distribute the force exerted by the pressure...

7/3,K/8 (Item 8 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00839520

System and method for profiling code on symmetric multiprocessor architectures

System und Verfahren zum Profilieren von Code fur eine symmetrische Multiprozessorarchitektur

Systeme et procede d'etablissement du profil de code pour une architecture symetrique de multiprocesseur

PATENT ASSIGNEE:

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ATTORNEY REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 777181 A1 970604 (Basic)

EP 777181 B1 010822

APPLICATION (CC, No, Date): EP 96308586 961127;

PRIORITY (CC, No, Date): US 563334 951128

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/34

NOTE:

Figure number on first page: NONE

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB97	778
CLAIMS B	(English)	200134	818
CLAIMS B	(German)	200134	848
CLAIMS B	(French)	200134	1056
SPEC A	(English)	EPAB97	4616
SPEC B	(English)	200134	4698
Total word count - document A			5395
Total word count - document B			7420
Total word count - documents A + B			12815

...SPECIFICATION threads to sample performance metrics before and after certain code regions. In addition, the profiler uses extensions to a

parallel support layer to register a parent thread with its child threads. Each thread stores the measured performance metric, or delta, in a memory cell or cells corresponding to its region and its parent region. When the process is complete, the profiler...

...SPECIFICATION threads to sample performance metrics before and after certain code regions. In addition, the profiler uses extensions to a parallel support layer to register a parent thread with its child threads. Each thread stores the measured performance metric, or delta, in a memory cell or cells corresponding to its region and its parent region. When the process is complete, the profiler...

7/3,K/9 (Item 9 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00789101

Method and apparatus for managing a database in a distributed object operating environment

Verfahren und Gerat zum Verwalten einer Datenbank in einer verteilten Objektbetriebsumgebung

Methode et appareil pour gerer une base de donnees dans un environnement d'exploitation d'objets distribue

PATENT ASSIGNEE:

SUN MICROSYSTEMS, INC., (1392732), 2550 Garcia Avenue, Mountain View, California 94043-1100, (US), (Proprietor designated states: all)

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PATENT (CC, No, Kind, Date): EP 735473 A2 961002 (Basic)
EP 735473 A3 980204
EP 735473 B1 030903

APPLICATION (CC, No, Date): EP 96301249 960223;

PRIORITY (CC, No, Date): US 414119 950331

DESIGNATED STATES: DE; FR; GB; IT; SE

INTERNATIONAL PATENT CLASS: G06F-009/46; G06F-012/08

ABSTRACT WORD COUNT: 220

NOTE:

Figure number on first page: NONE

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200336	1499
CLAIMS B	(German)	200336	1232
CLAIMS B	(French)	200336	1895
SPEC B	(English)	200336	10961
Total word count - document A			0
Total word count - document B			15587
Total word count - documents A + B			15587

...SPECIFICATION thread may lock a mutex corresponding to a certain condition variable having a data structure which includes a true or false flag, thereby preventing all other threads from operating on this condition variable. The first thread may then evaluate the true or false flag, performing the desired action if the flag value is true. If the flag is false, the thread blocks on the...

7/3,K/10 (Item 10 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00604068

Textile support for fusible interlining having air-jet texturised weft yarns

Textilsupport für einbugelbaren Einlagestoff mit lufttexturierten Schussfaden

Support textile pour entoilage thermocollant comportant en trame des fils textures par jet d'air

PATENT ASSIGNEE:

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AT; BE; CH; DE; DK; ES; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE)

INVENTOR:

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(FR)

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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 578527 A1 940112 (Basic)

EP 578527 B1 970521

APPLICATION (CC, No, Date): EP 93401621 930624;

PRIORITY (CC, No, Date): FR 928952 920706

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; GB; GR; IE; IT; LI; LU; MC; NL;
PT; SE

INTERNATIONAL PATENT CLASS: D02G-001/16; D03D-015/00;

TRANSLATED ABSTRACT WORD COUNT: 96

ABSTRACT WORD COUNT: 96

LANGUAGE (Publication,Procedural,Application): French; French; French

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(French)	EPABF2	235
CLAIMS B	(English)	EPAB97	249
CLAIMS B	(German)	EPAB97	201
CLAIMS B	(French)	EPAB97	240
SPEC A	(French)	EPABF2	2801
SPEC B	(French)	EPAB97	2799
Total word count - document A			3037
Total word count - document B			3489
Total word count - documents A + B			6526

...ABSTRACT of which is made from synthetic threads of high bulk, obtained by the air-jet texturing technique from at least two multifilament threads, namely a **first core thread** at 20 to 40 % by **weight** and a **second fancy thread** at 60 to 80 % by **weight**, the overfeed of the core thread being between 10 and 25 % and the overfeed of the fancy thread being at least 70 %. ...

7/3,K/11 (Item 11 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00562836

Tapper for forming internal threads in a precision frustoconical hole in bones

Gewindeschneideinrichtung zur Formung eines Innengewindes in einer genau kegelstumpfformigen Bohrung eines Knochens

Outil de filetage pour former un filet femelle dans une forure tronconique precise d'un os

PATENT ASSIGNEE:

Vrespa, Giuseppe, (403101), Via C.Battisti 26, I-20025 Legnano (Milan),
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AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE)

INVENTOR:

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LEGAL REPRESENTATIVE:

Giambrocono, Alfonso, Dr. Ing. et al (40521), Ing. A. Giambrocono & C.

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 PATENT (CC, No, Kind, Date): EP 554915 A1 930811 (Basic)
 EP 554915 B1 961211
 APPLICATION (CC, No, Date): EP 93102625 901011;
 PRIORITY (CC, No, Date): IT 8922139 891026
 DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE
 RELATED PARENT NUMBER(S) - PN (AN):
 EP 424734 (EP 901194597)
 INTERNATIONAL PATENT CLASS: A61B-017/16;
 ABSTRACT WORD COUNT: 98

LANGUAGE (Publication,Procedural,Application): English; English; English
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	259
CLAIMS B	(English)	EPAB96	251
CLAIMS B	(German)	EPAB96	234
CLAIMS B	(French)	EPAB96	268
SPEC A	(English)	EPABF1	9033
SPEC B	(English)	EPAB96	6092
Total word count - document A			9293
Total word count - document B			6845
Total word count - documents A + B			16138

...SPECIFICATION the self-tapping type, and intended to fix into that cortical part of the bone opposite the part into which the screw is inserted, said **second thread** having a number of starts which is a multiple of that of the **first thread**.

In **contrast** to a screw with a cylindrical core, a screw with a frusto-conical core, because of its particular geometrical shape and if associated with a...

7/3,K/12 (Item 12 from file: 348)
 DIALOG(R)File 348:EUROPEAN PATENTS
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00562835

Cutter for enabling to form a precision frustoconical hole in bones.
 Schneidgerät zur Formung einer genau kegelstumpfförmigen Bohrung in einem Knochen.

Outil coupant pour former une forure tronconique precise dans un os.

PATENT ASSIGNEE:

Vrespa, Giuseppe, (403101), Via C.Battisti 26, I-20025 Legnano, (IT),
 (applicant designated states:
 AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;LU;NL;SE)

INVENTOR:

Vrespa, Giuseppe, Via C.Battisti 26, I-20025 Legnano, (IT)

LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 557899 A1 930901 (Basic)

APPLICATION (CC, No, Date): EP 93102624 901011;

PRIORITY (CC, No, Date): IT 8922139 891026

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 424734 (EP 901194597)

INTERNATIONAL PATENT CLASS: A61B-017/16;

ABSTRACT WORD COUNT: 102

LANGUAGE (Publication,Procedural,Application): English; English; English
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	55
SPEC A	(English)	EPABF1	9011
Total word count - document A			9066
Total word count - document B			0
Total word count - documents A + B			9066

...SPECIFICATION the self-tapping type, and intended to fix into that cortical part of the bone opposite the part into which the screw is inserted, said **second thread** having a number of starts which is a multiple of that of the **first thread**.

In **contrast** to a screw with a cylindrical core, a screw with a frusto-conical core, because of its particular geometrical shape and if associated with a...

7/3,K/13 (Item 13 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00557286

RUGGEDIZED FLASHLAMP EXHIBITING HIGH AVERAGE POWER AND LONG LIFE.
VERSTÄRKT BLITZLAMPE MIT HOHER MITTLERER LEISTUNG UND LANGER LEBENSDAUER.
LAMPE ECLAIR A ROBUSTESSE ELEVEE AYANT UNE PUISSANCE MOYENNE ELEVEE ET UNE
LONGUE VIE UTILE.

PATENT ASSIGNEE:

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AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;LU;MC;NL;SE)

INVENTOR:

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CANNALLY, William, James, 8808 Calliandra Road, San Diego, CA 92126, (US)

LEGAL REPRESENTATIVE:

Cross, Rupert Edward Blount et al (42891), BOULT, WADE & TENNANT 27
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PATENT (CC, No, Kind, Date): EP 568642 A1 931110 (Basic)
EP 568642 B1 951102
WO 9213358 920806

APPLICATION (CC, No, Date): EP 92906025 920122; WO 92US535 920122

PRIORITY (CC, No, Date): US 645372 910124

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; MC; NL;
SE

INTERNATIONAL PATENT CLASS: H01J-061/80; H01J-061/90; H01J-061/36;

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB95	569
CLAIMS B	(German)	EPAB95	535
CLAIMS B	(French)	EPAB95	599
SPEC B	(English)	EPAB95	5451
total word count - document A			0
total word count - document B			7154
total word count - documents A + B			7154

...CLAIMS through the use of:

an attachment ring (16) affixed to the first and second open ends of the transparent tube, said attachment ring having a **first set of threads** (22) thereon,

a **second set of threads** (30) on said end caps adapted to **match** said **first set of threads**, whereby said end cap may be threaded onto said attachment ring, and

a soft metal ring (38) inserted between engaging members (40, 42) of said...

7/3,K/14 (Item 14 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00435180

Parallel processing trace data manipulation
Bearbeitung von Ablaufdaten paralleler Verarbeitung

Manipulation de donnees trace de traitement en parallele

PATENT ASSIGNEE:

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INVENTOR:

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LEGAL REPRESENTATIVE:

Burt, Roger James, Dr. et al (52152), IBM United Kingdom Limited
Intellectual Property Department Hursley Park, Winchester Hampshire
SO21 2JN, (GB)

PATENT (CC, No, Kind, Date): EP 422945 A2 910417 (Basic)
EP 422945 A3 920304
EP 422945 B1 960814

PUBLICATION (CC, No, Date): EP 90311178 901011;

PRIORITY (CC, No, Date): US 420845 891013

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/32; G06F-011/34;

ABSTRACT WORD COUNT: 63

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB96	525
CLAIMS B	(German)	EPAB96	579
CLAIMS B	(French)	EPAB96	611
SPEC B	(English)	EPAB96	5484
Total word count - document A			0
Total word count - document B			7199
Total word count - documents A + B			7199

...CLAIMS backward view expressed as a time range or interval, and said mapping step further includes the steps of expressing parallel activity in the form of **primary** and **secondary threads**, a parallel thread being a unit of work eligible for concurrent execution, **comparing** the beginning and end times for each primary thread to the time range of the selected view, and selectively adjusting the view to conform the...

7/3,K/37 (Item 18 fr file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00542546 **Image available**

TELECOMMUNICATIONS SWITCHING SYSTEM UTILIZING A CHANNELIZED DATABASE ACCESS MECHANISM

SYSTEME DE COMMUTATION DE TELECOMMUNICATIONS UTILISANT UN MECANISME D'ACCES DE BASES DE DONNEES A TOPOGRAPHIE EN CANAL

Patent Applicant/Assignee:

PRIORITY CALL MANAGEMENT INC,
MELAMPY Patrick J,
PENFIELD Robert F,
RYAN Theresa H,
FISHER Duncan S,

Inventor(s):

MELAMPY Patrick J,
PENFIELD Robert F,
RYAN Theresa H,
FISHER Duncan S,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200005919 A1 20000203 (WO 0005919)
Application: WO 99US3760 19990219 (PCT/WO US9903760)
Priority Application: US 98122148 19980724

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES
FI GB GE GH GM HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG
MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ
VN YU ZW GH GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH
CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW
ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 16787

Fulltext Availability:

Detailed Description

Detailed Description

... an available child thread 3 1 0 to process the database transaction,
the packet is added to the wait queue and handed off to the **child
thread 3 1 0** for processing.

The. **parent / child thread 308** polls the **child thread 3 1 0** for
the returned response from processing a DbTransaction. The **parent /
child thread 308** uses the sequence number to **match** the response with
the XCP packet on the wait queue. If the XCP packet has already
timed-out, the sequence number will not match any...